Trace Based Switching For A Tightly Coupled Heterogeneous Core Shruti Padmanabha, Andrew Lukefahr, Reetuparna Das, Scott Mahlke **University of Michigan** compilers creating custom processors **Electrical Engineering and Computer Science Fine-grained Heterogeneity** Traditional Switching Controllers Traditional big.LITTLE Architecture Composite Cores Architecture - Traditional Controller ←Oracle Big % Time Spent On Little Little Big 60% Little Backend Backend 50% Share Architectura 40% Shared State 30% Frontend 20% Memory Memory 10% 0% Transfer Overhead: ~20K cycles Transfer Overhead: ~35 cycles 100 10K 100K 1K 1M **#** Instructions per Quantum Super-trace Construction Fine-granularity "Reacts" Poorly

similar to recent past

Don't React – Predict!



