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# SAGE: Self-Tuning Approximation for Graphics Engines

**Mehrzed Samadi<sup>1</sup>, Janghaeng Lee<sup>1</sup>, D. Anoushe  
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University of Michigan<sup>1</sup>, Google Inc.<sup>2</sup>

December 2013

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University of Michigan  
Electrical Engineering and Computer Science

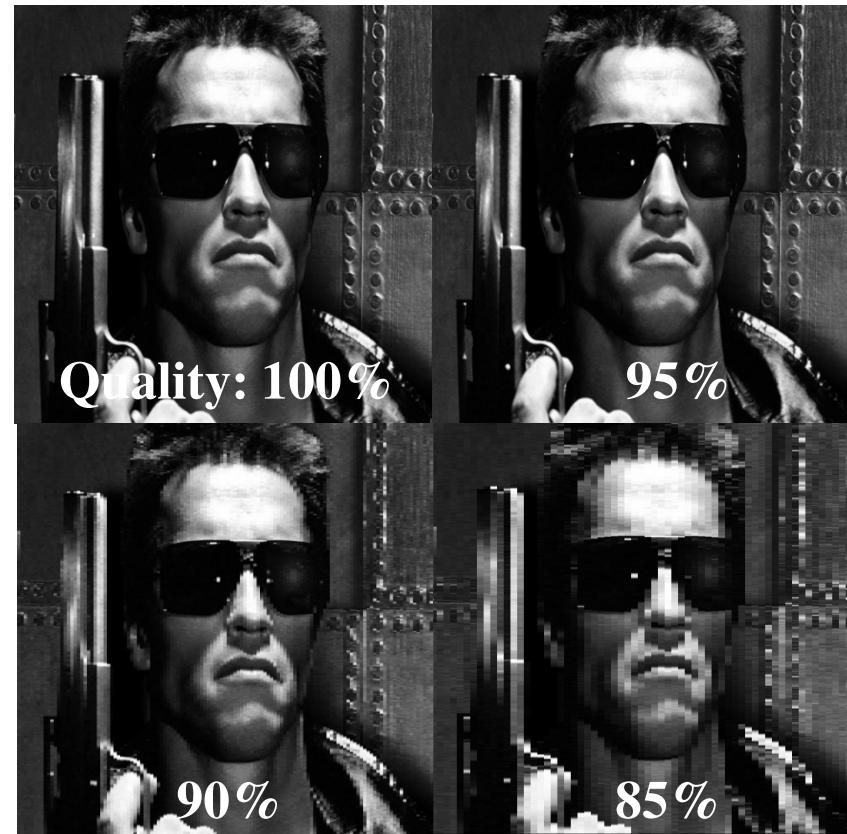


Compilers creating custom processors

# Approximate Computing

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- Different domains:
  - Machine Learning
  - Image Processing
  - Video Processing
  - Physical Simulation
  - ...



Less work



Higher performance  
Lower power consumption

# Ubiquitous Graphics Processing Units

- Wide range of devices



Super Computers



Servers



Desktops



Cell Phones

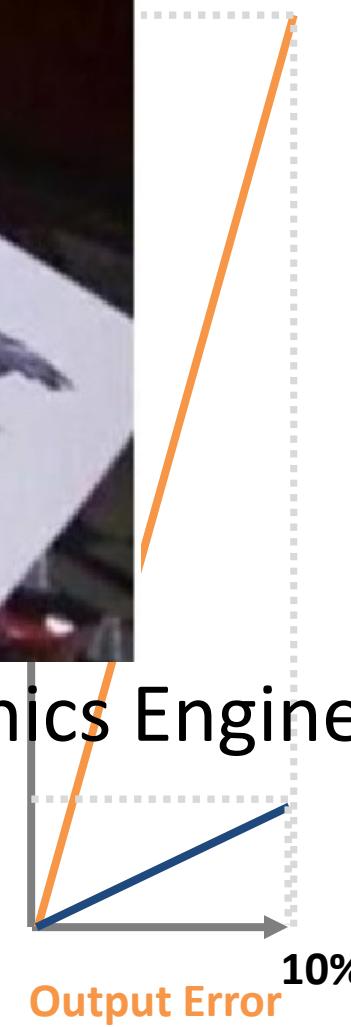


- Mostly regular applications
- Works on large data sets

Good opportunity for automatic approximation

# SAGE Framework

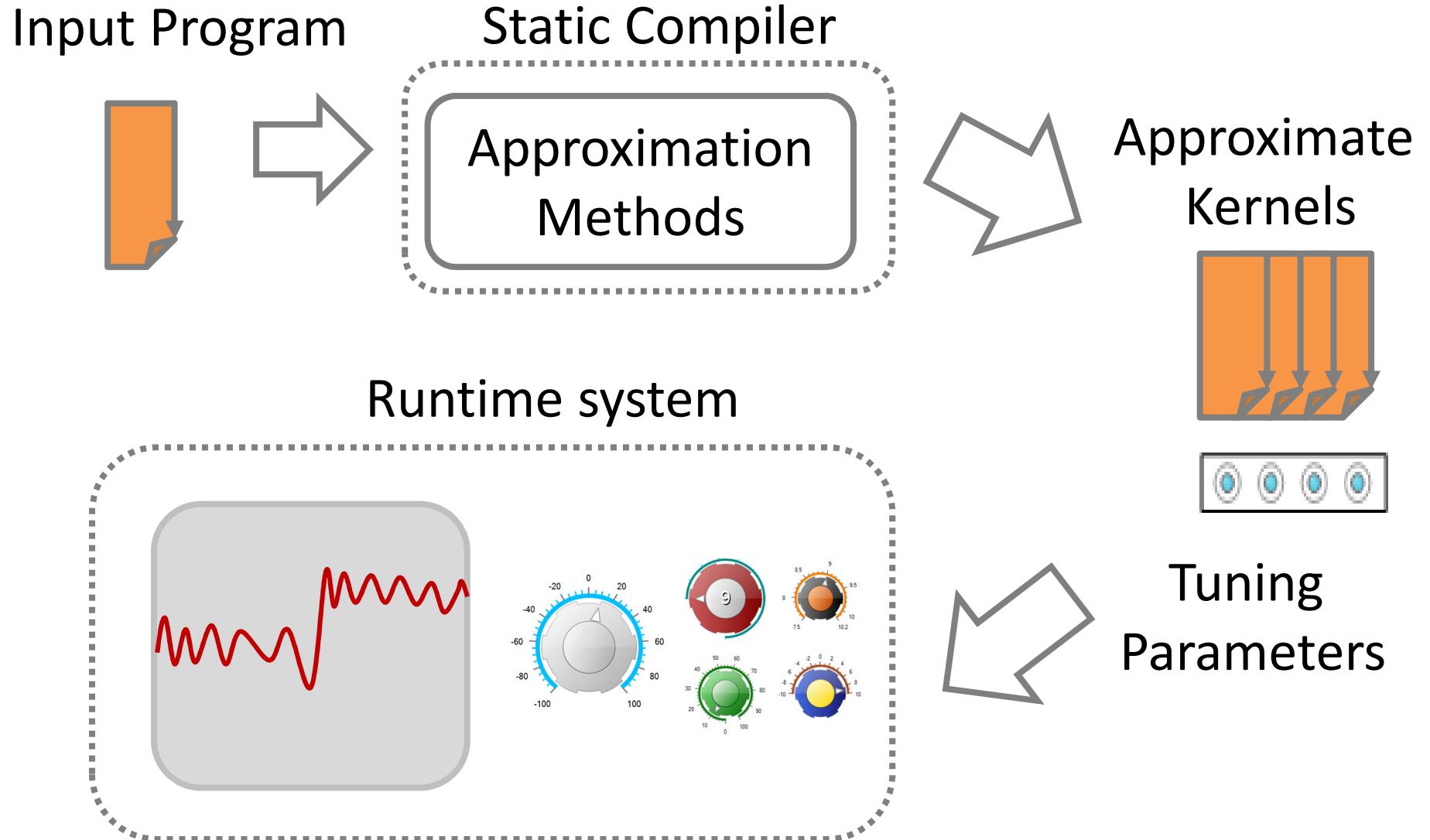
- Sir
- 
- 
- 
- Self-Tuning Approximation on Graphics Engines
  - Write the program once
  - Automatic approximation
  - Self-tuning dynamically



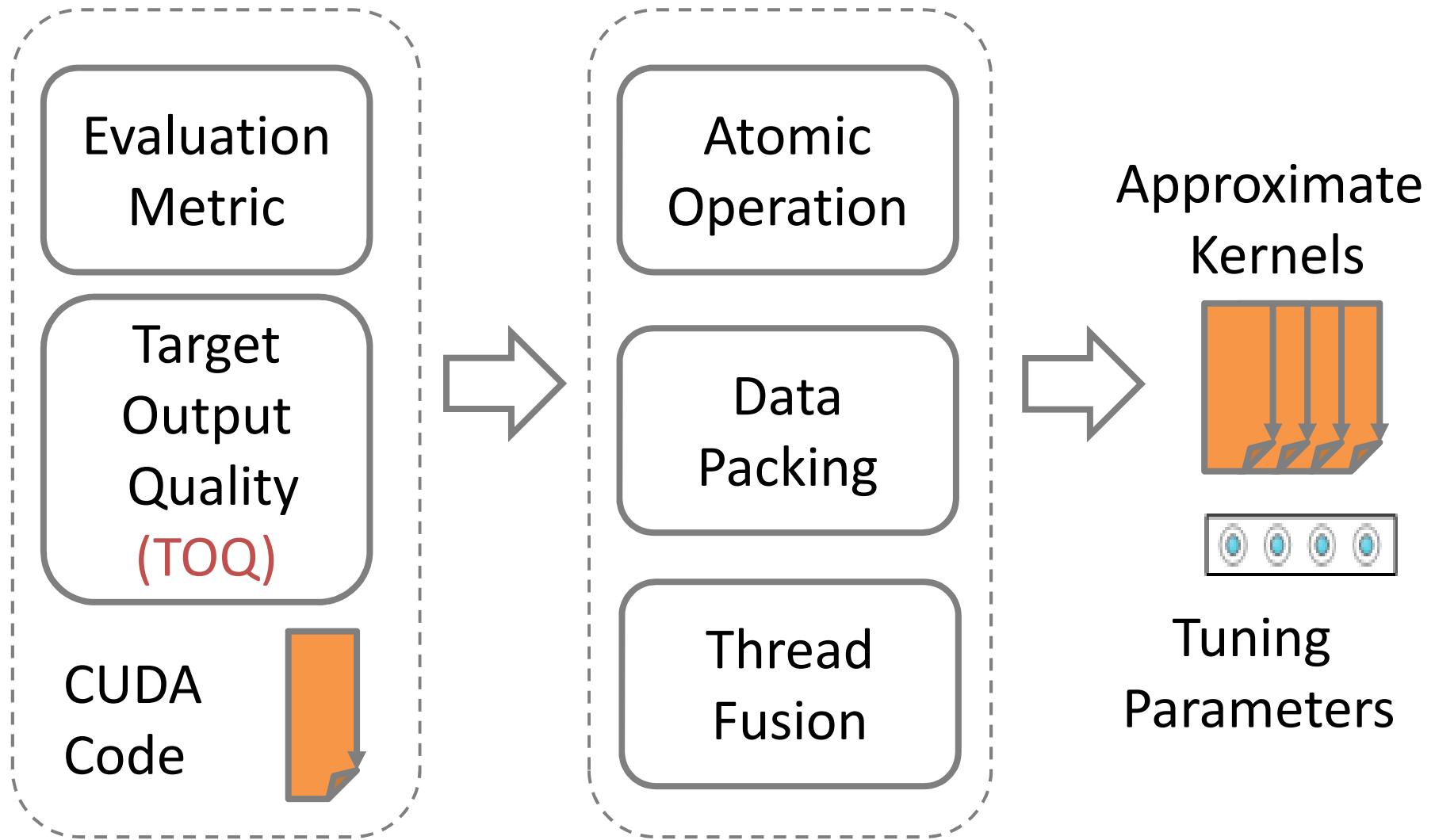
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# Overview

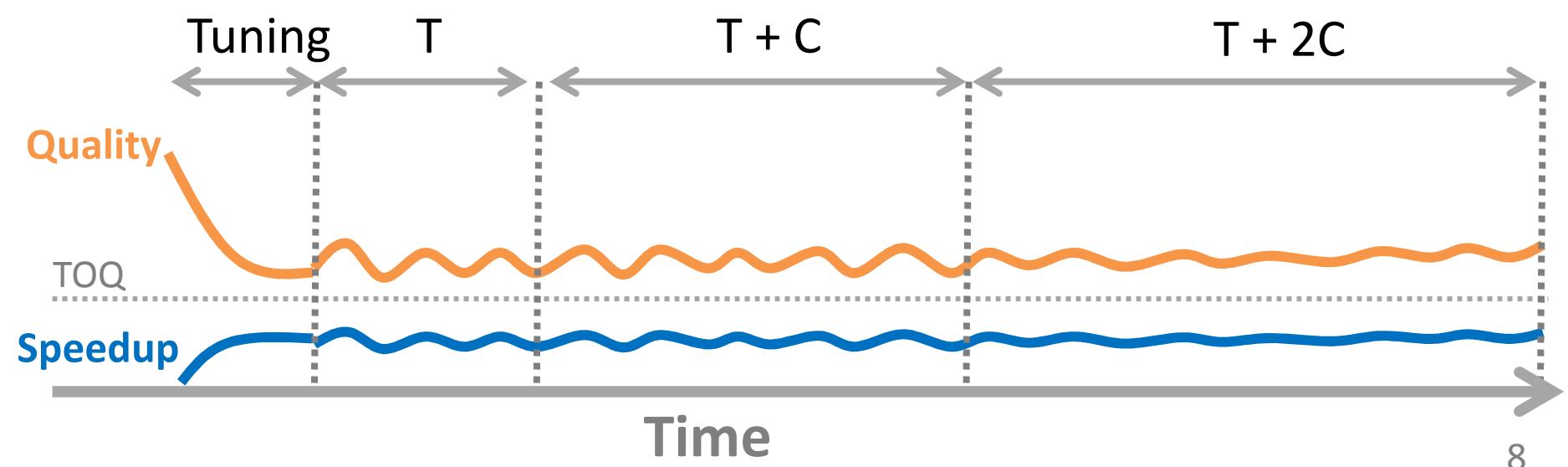
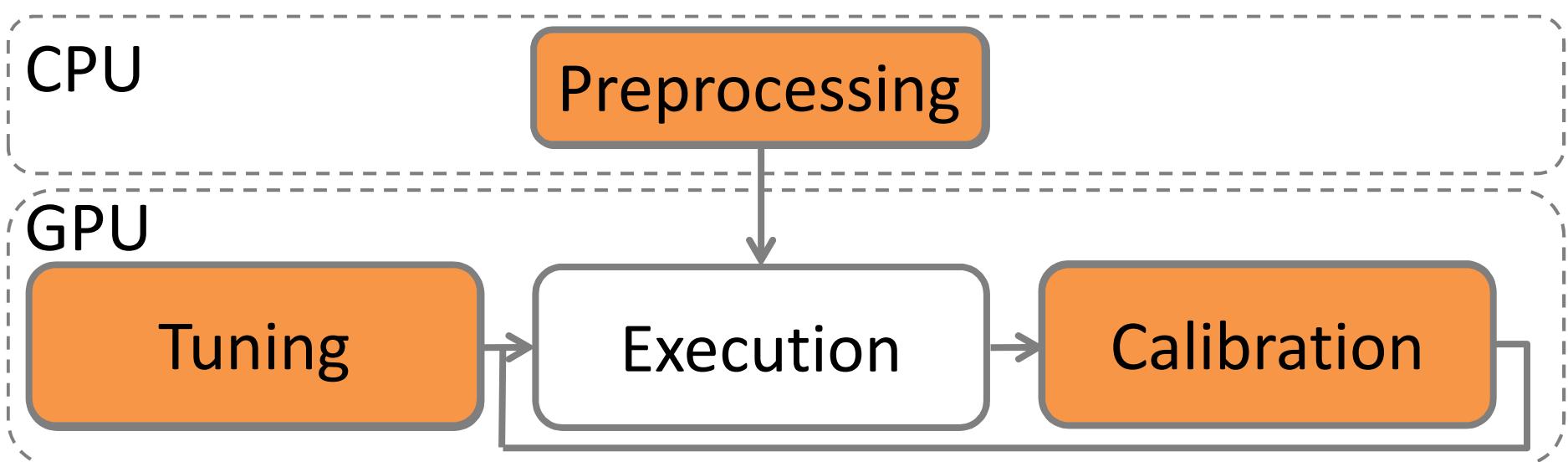
# SAGE Framework



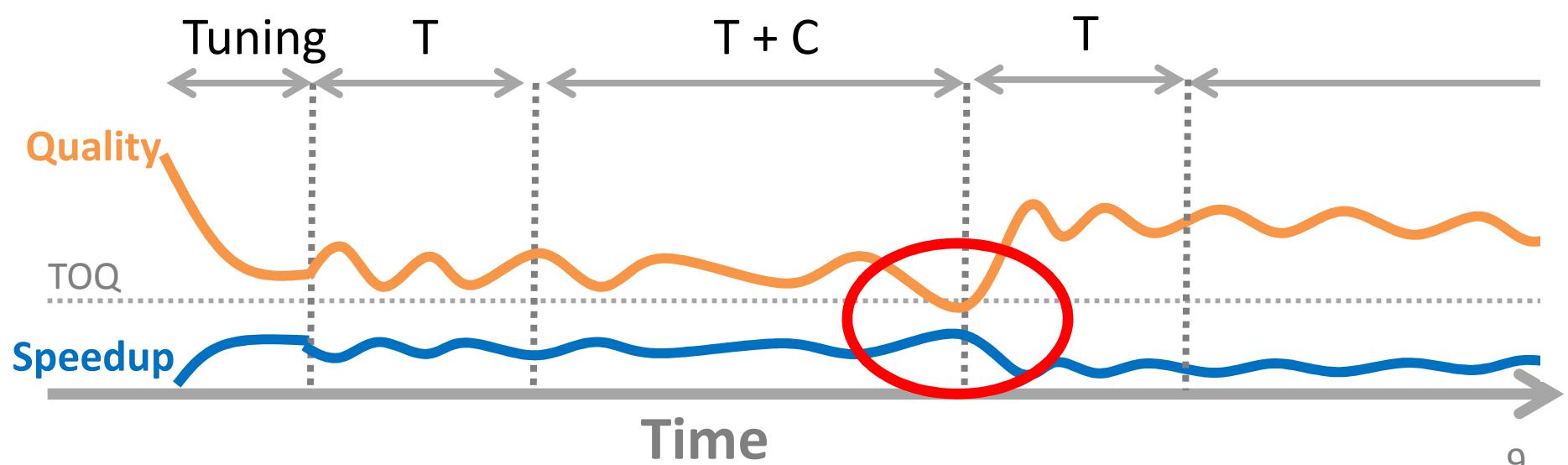
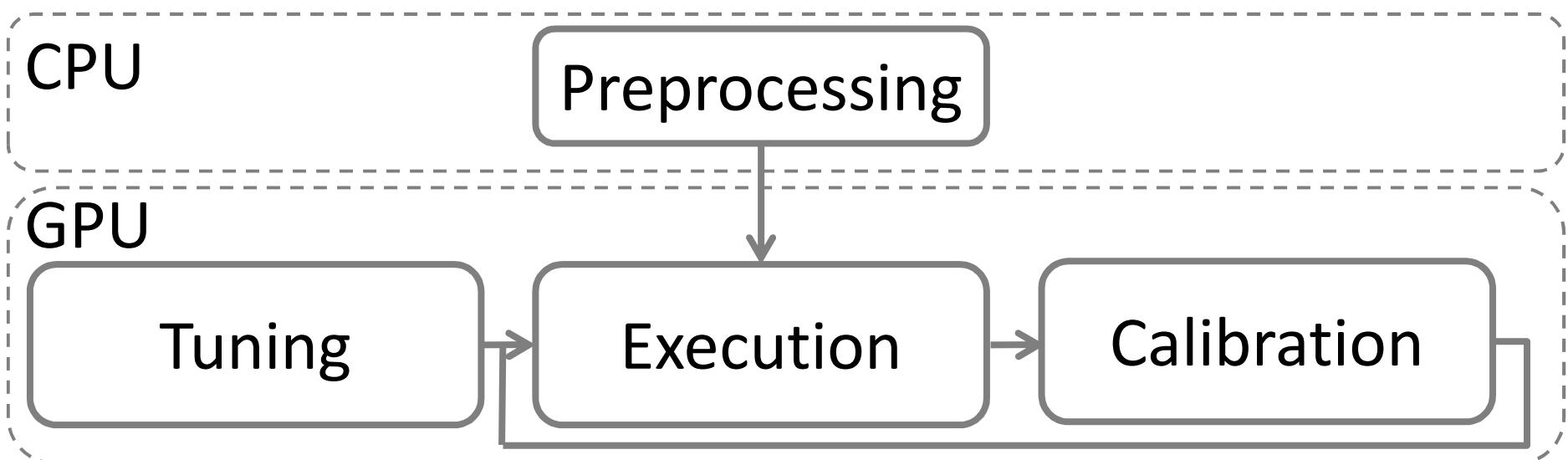
# Static Compilation



# Runtime System



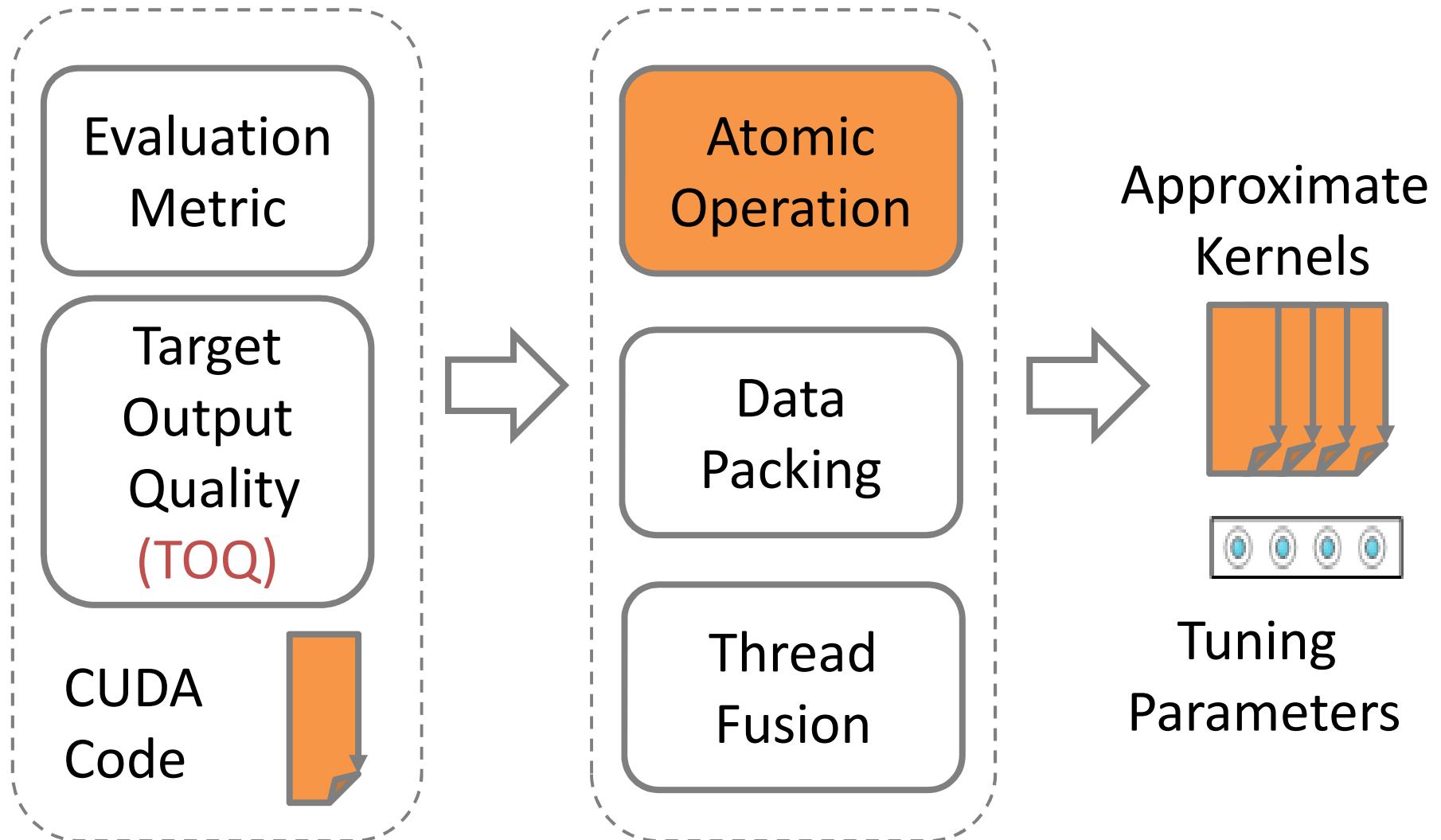
# Runtime System



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# Approximation Methods

# Approximation Methods



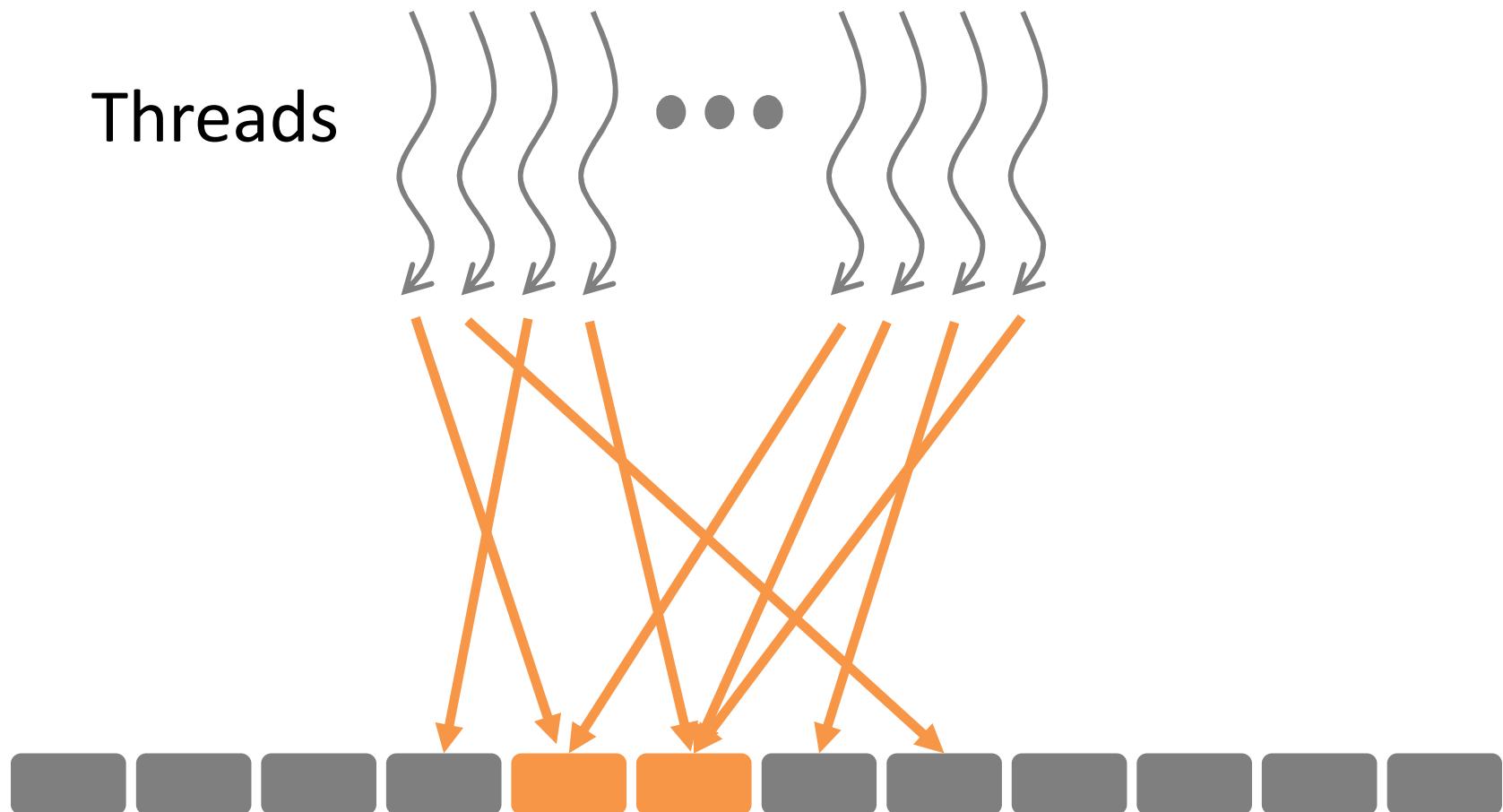
# Atomic Operations

- Atomic operations update a memory location such that the update appears to happen atomically

```
// Compute histogram of colors in an image
__global__ void histogram(int n, int* color, int* bucket)
int tid = threadIdx.x + blockDim.x * blockIdx.x;
int nThreads = gridDim.x * blockDim.x;
for ( int i = tid ; tid < n; tid += nThreads) ←
    int c = colors[i]; ←
    atomicAdd(&bucket[c], 1); ←
```

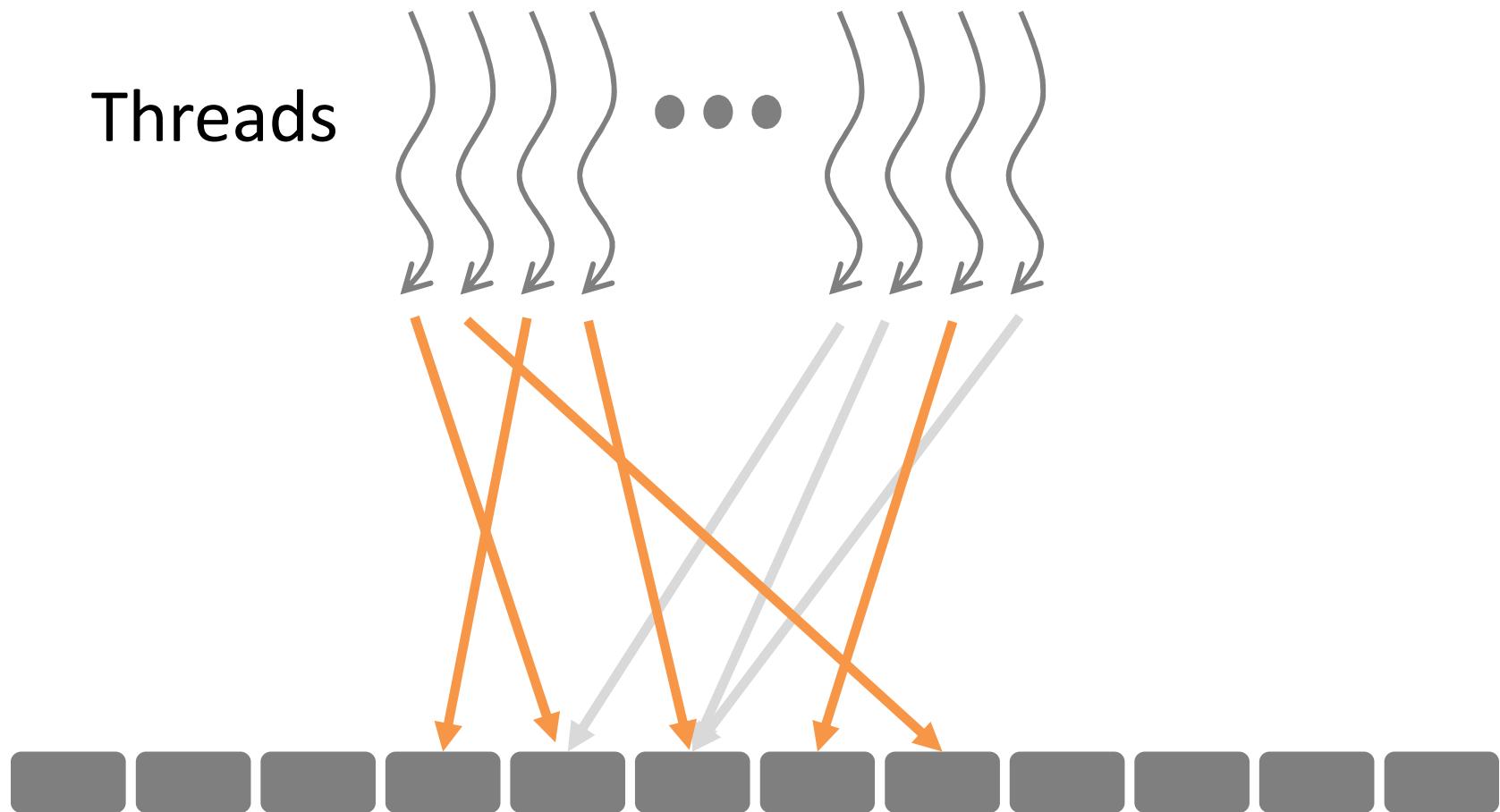
# Atomic Operations

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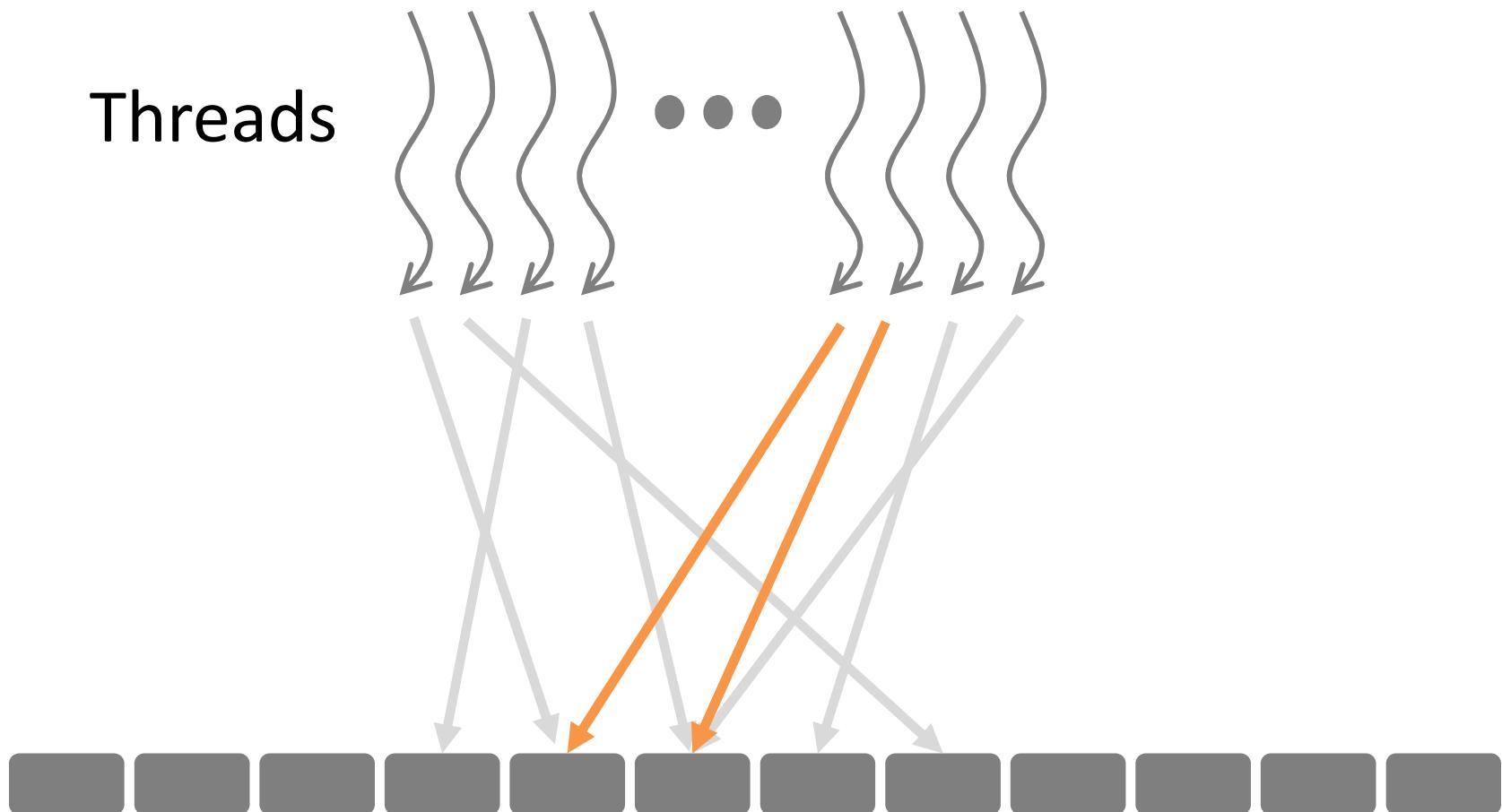
# Atomic Operations

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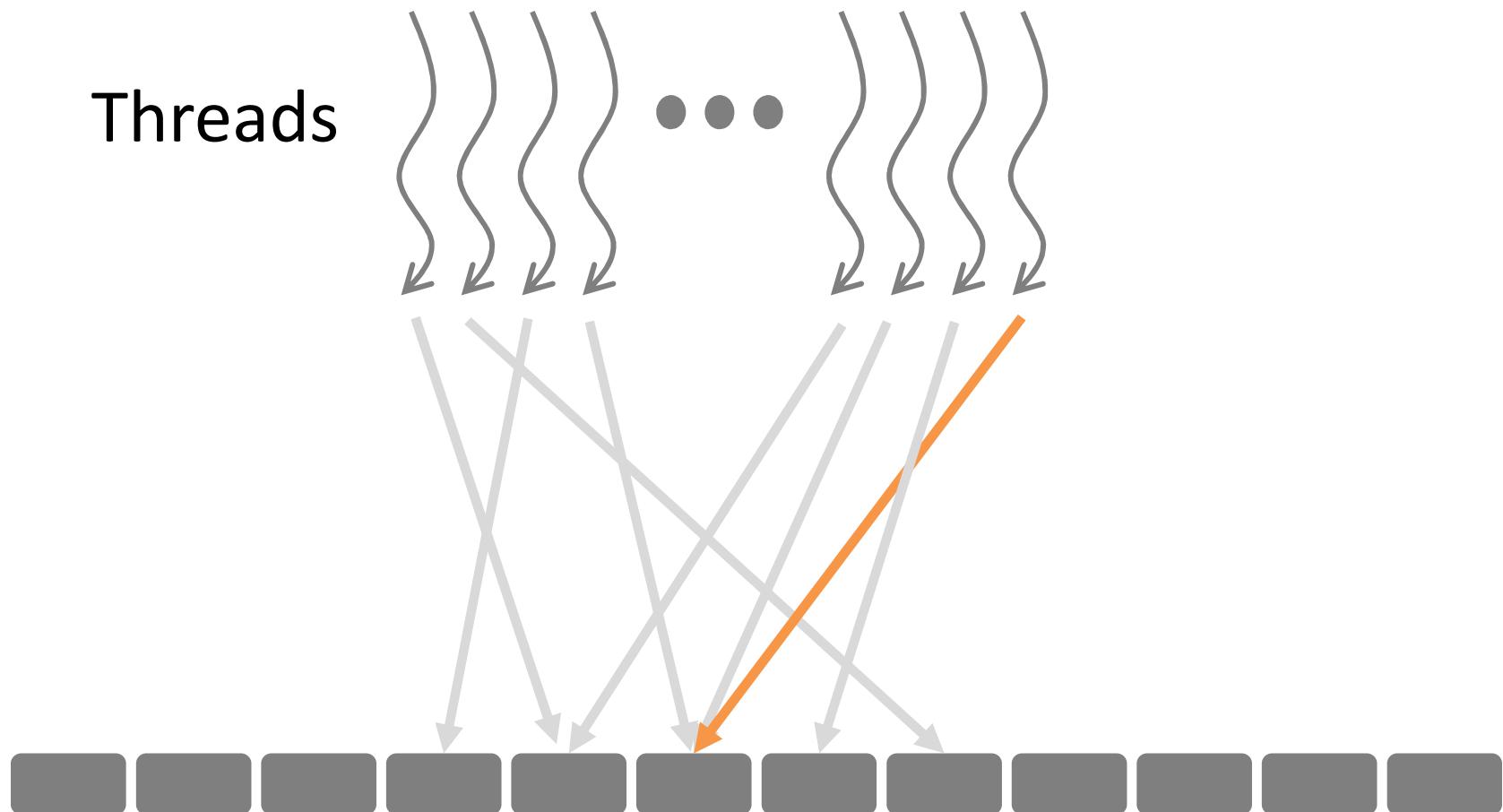
# Atomic Operations

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# Atomic Operations

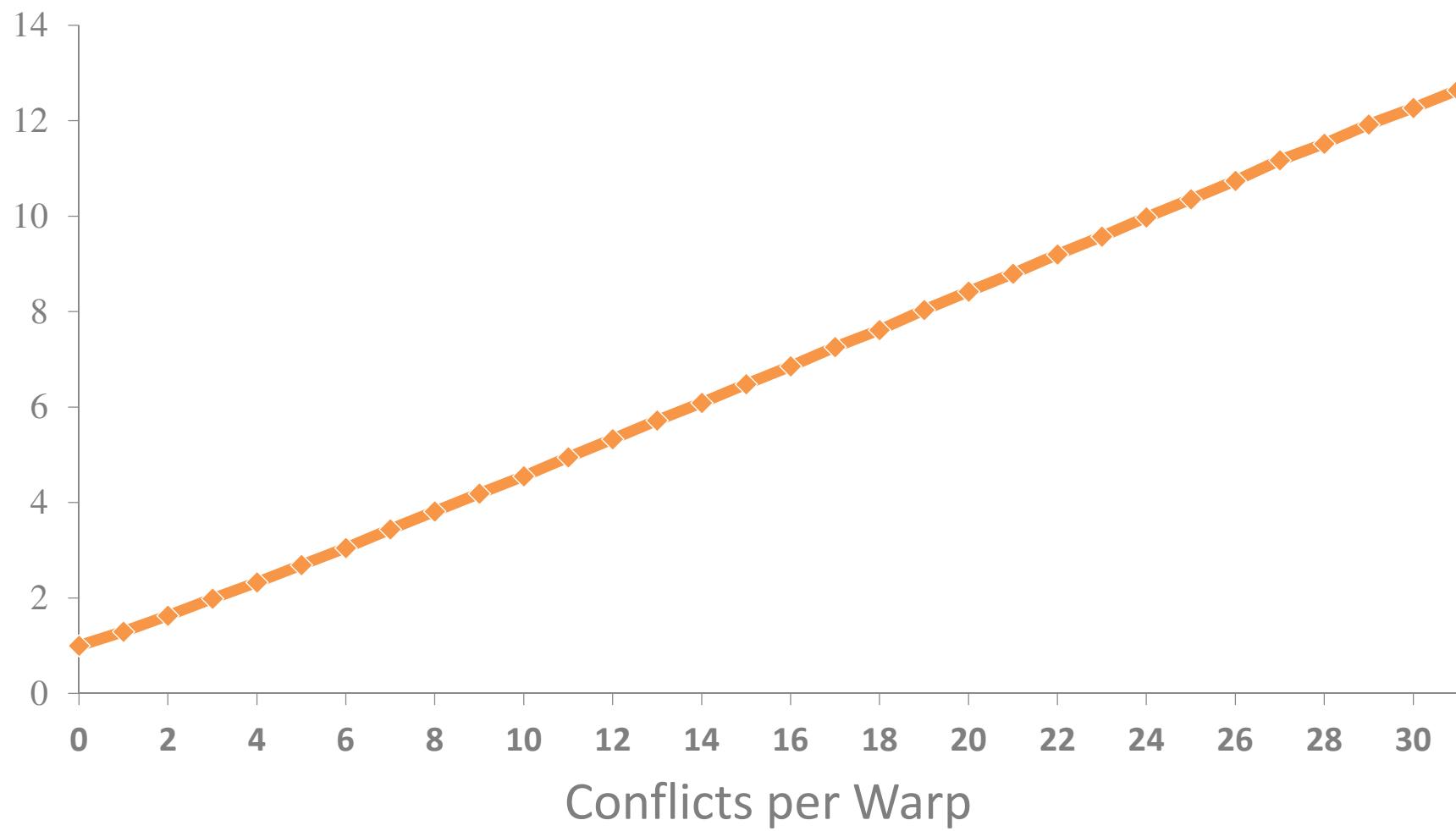
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# Atomic Add

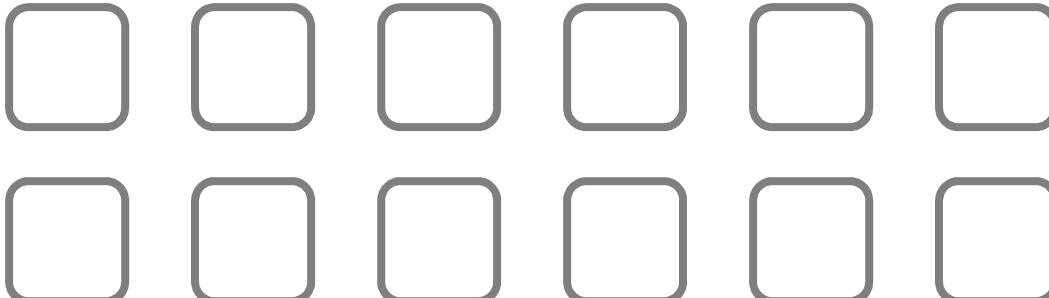
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Slowdown



# Atomic Operation Tuning

Iterations

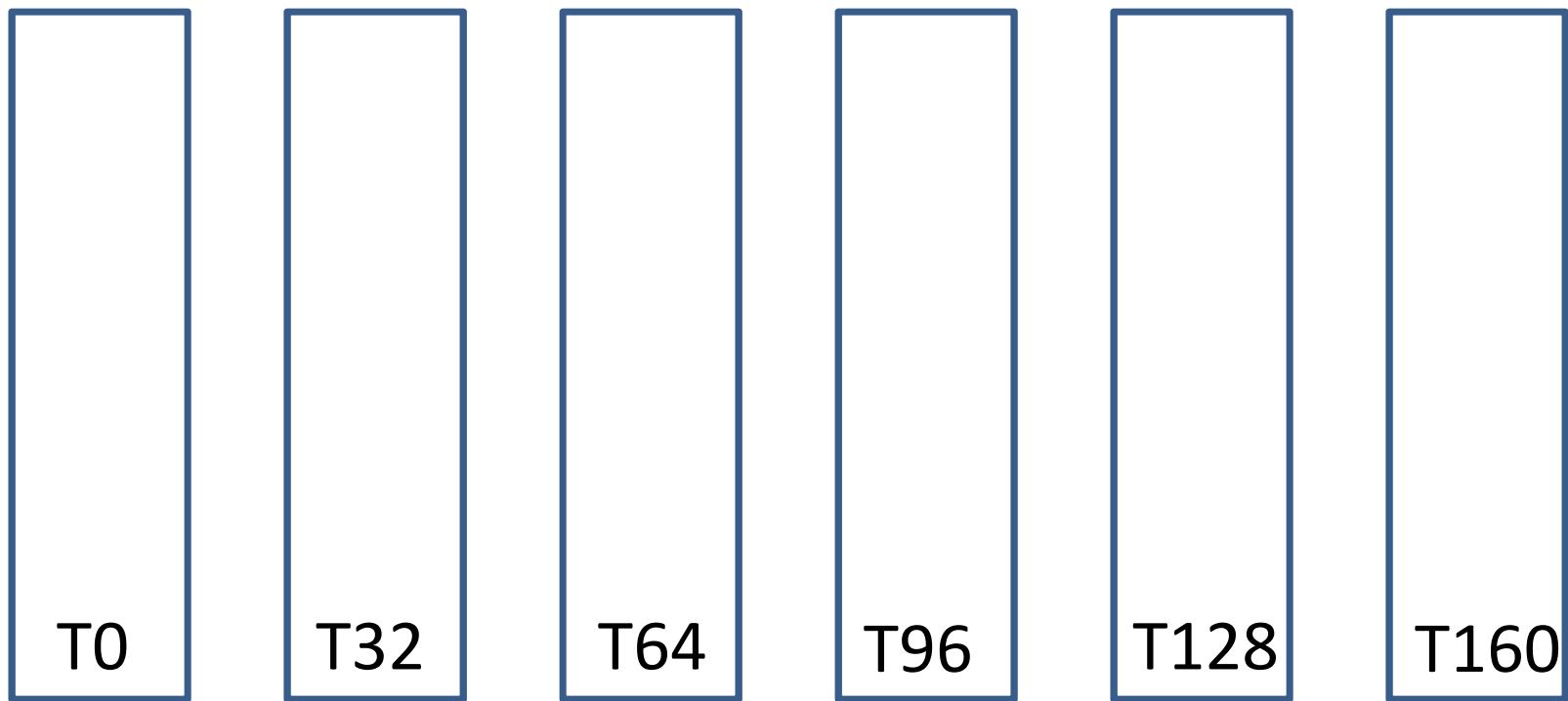


```
// Compute histogram of colors in an image
__global__ void histogram(int n, int* color, int* bucket)
{
    int tid = threadIdx.x + blockDim.x * blockIdx.x;
    int nThreads = gridDim.x * blockDim.x;
    for ( int i = tid ; tid < n; tid += nThreads)
        int c = colors[i];
        atomicAdd(&bucket[c], 1);
}
```

The code snippet shows a CUDA kernel for computing a histogram. It includes declarations for global memory, thread indices, and block dimensions. A red box highlights the loop where the atomic operation is performed. Below the code, the thread indices are mapped to specific values: T0, T32, T64, T96, T128, and T160.

# Atomic Operation Tuning

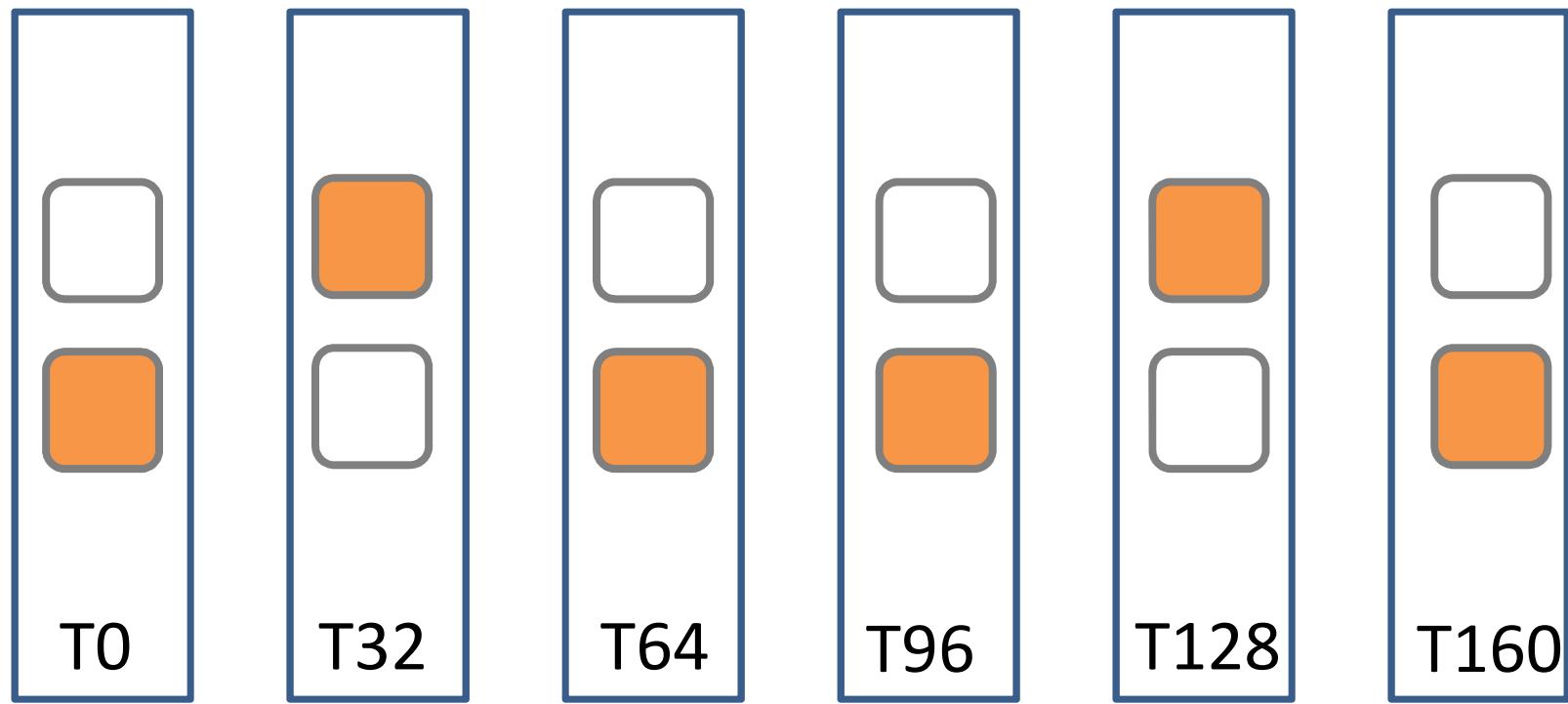
- SAGE skips one iteration per thread
- To improve the performance, it drops the iteration with the maximum number of conflicts



# Atomic Operation Tuning

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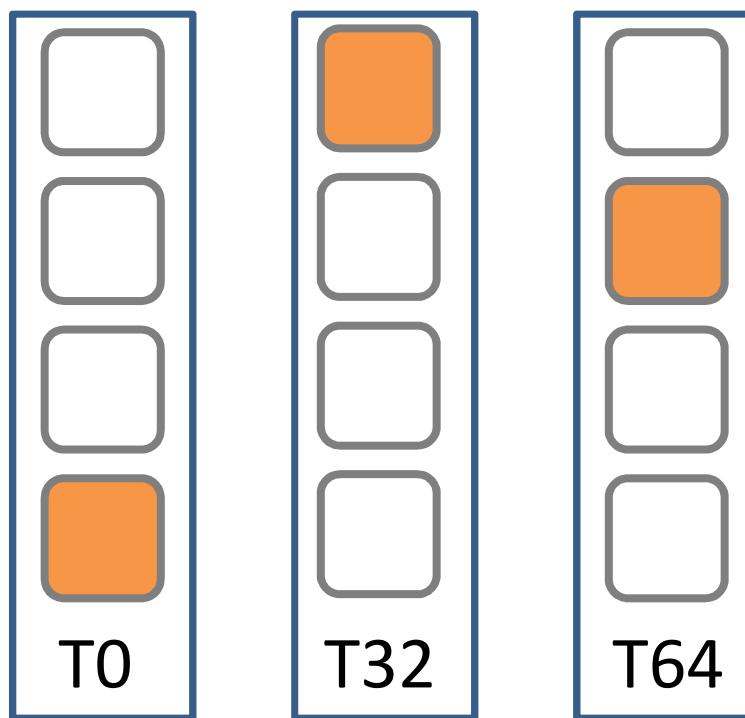
- SAGE skips one iteration per thread
- To improve the performance, it drops the iteration with the maximum number of conflicts  
**It drops 50% of iterations**



# Atomic Operation Tuning

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Drop rate goes down to 25%

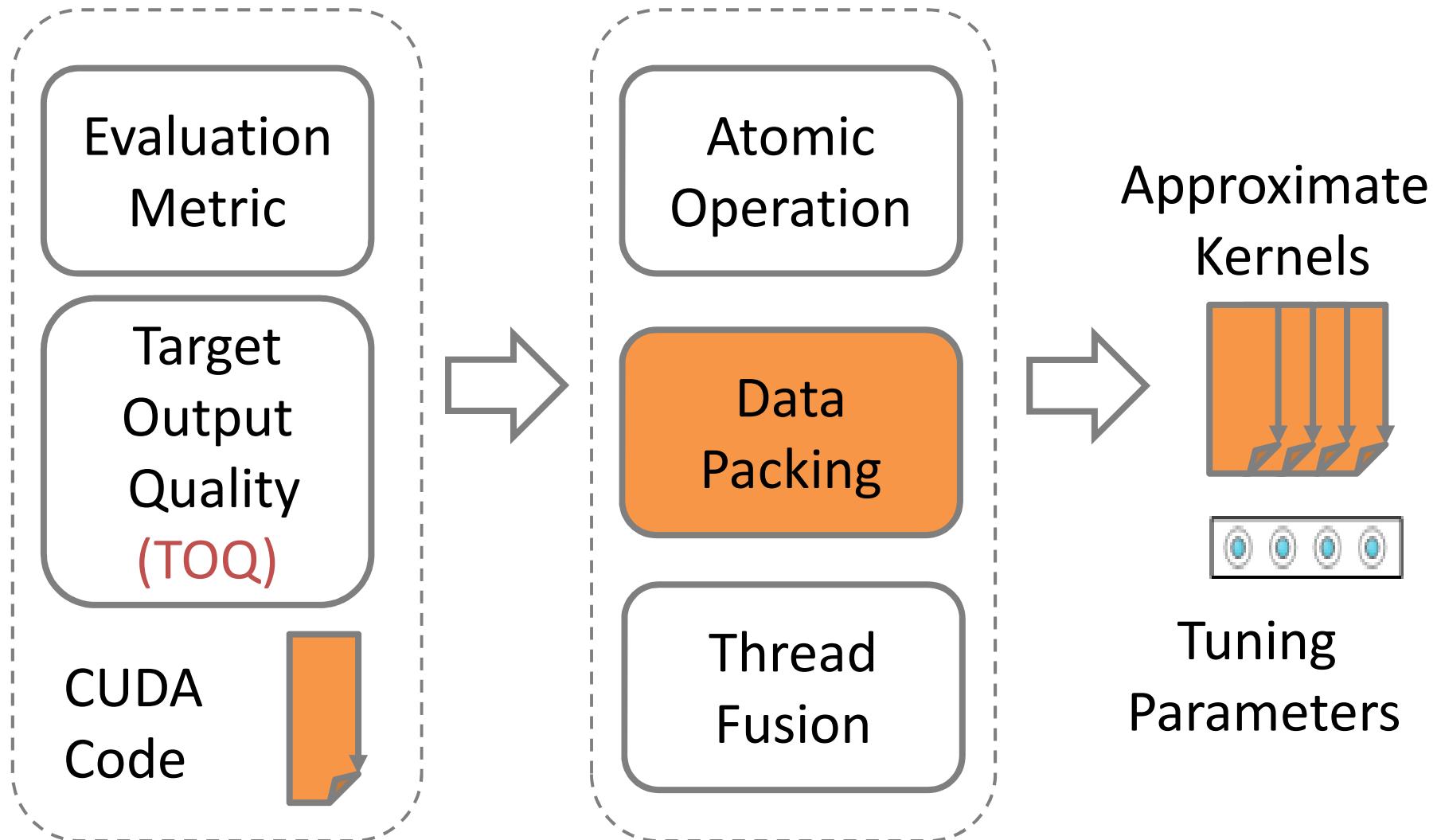


# Dropping One Iteration

Iteration No.	Conflicts	After optimization
0	2	CD 0 Conflict Detection
1	8	CD 1
2	17	0
3	12	CD 2
		1
		CD 3
		3

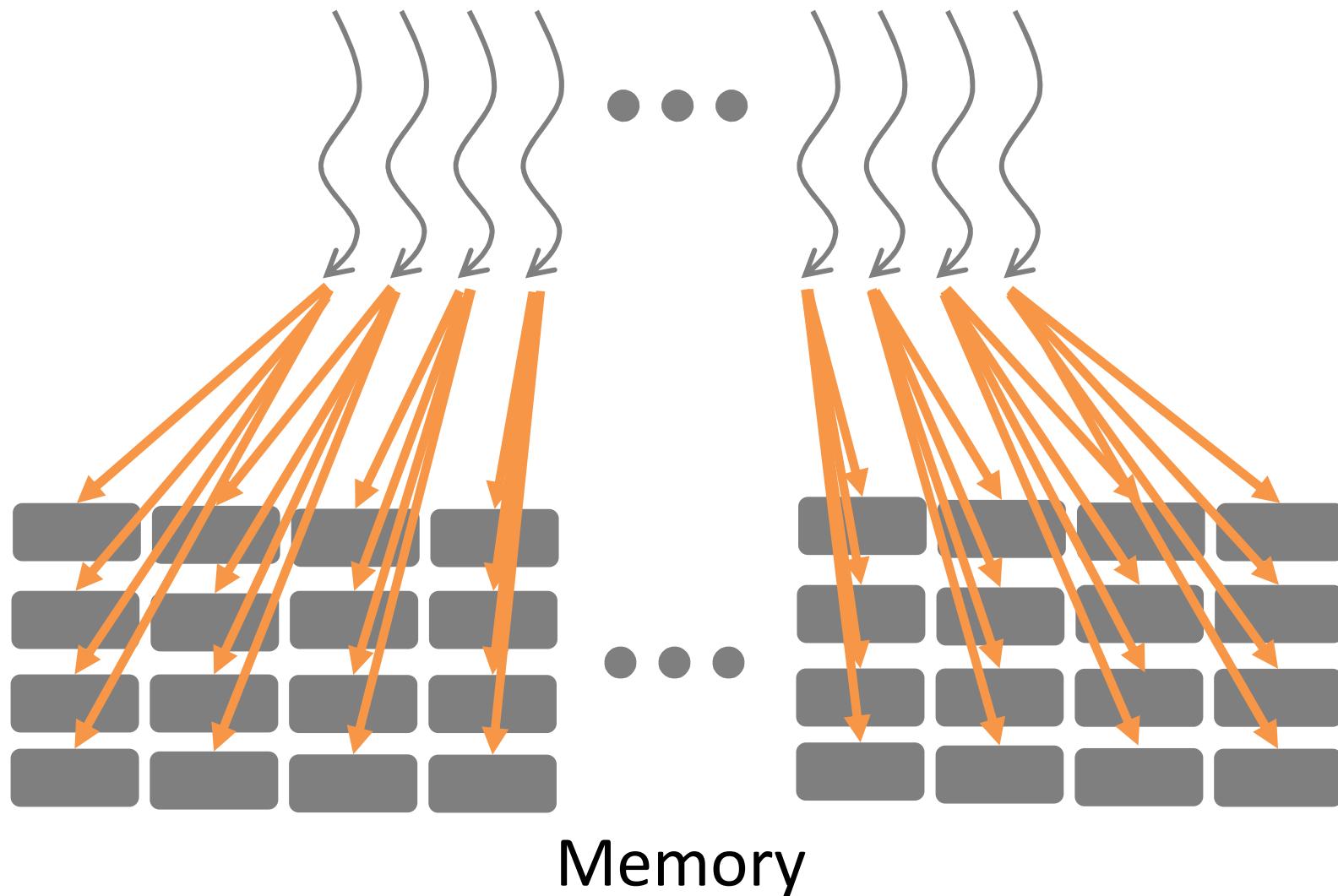
Max conflict  
**Iteration 2**

# Approximation Methods



# Data Packing

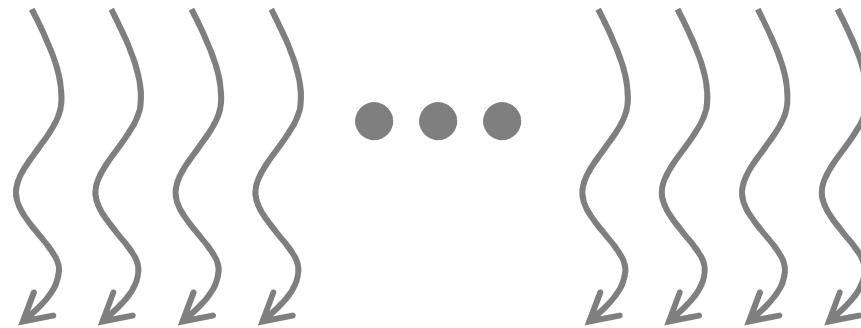
Threads



# Data Packing

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Threads

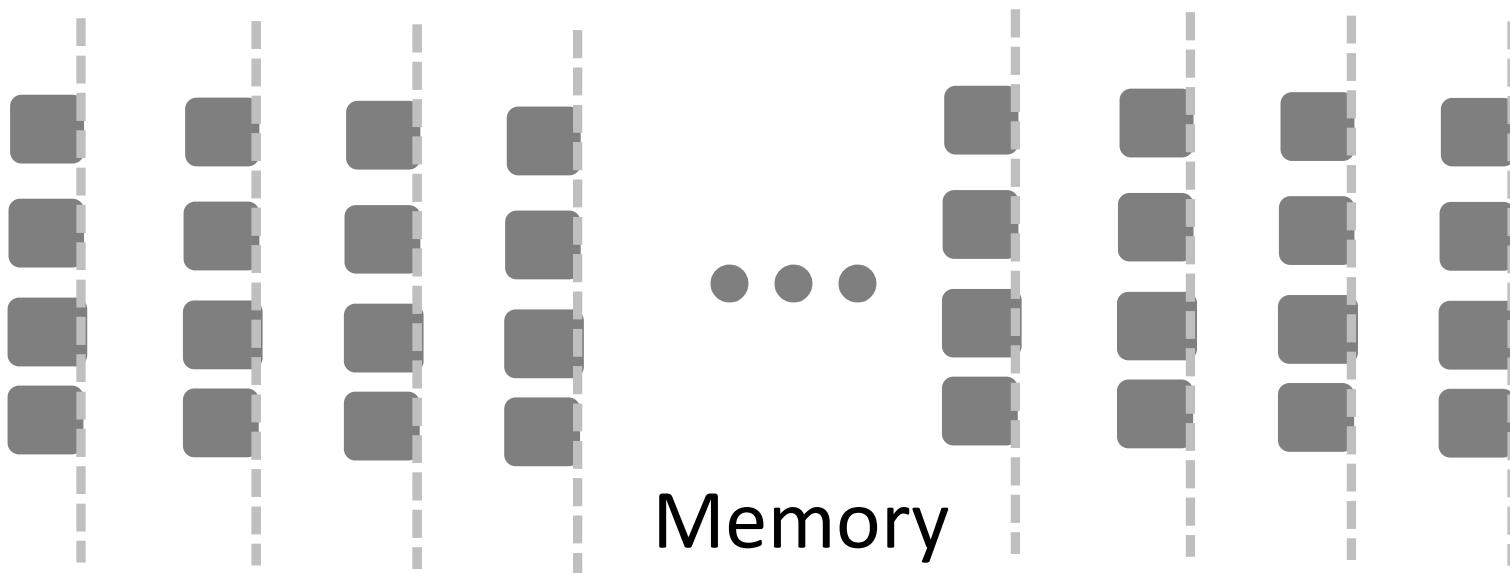
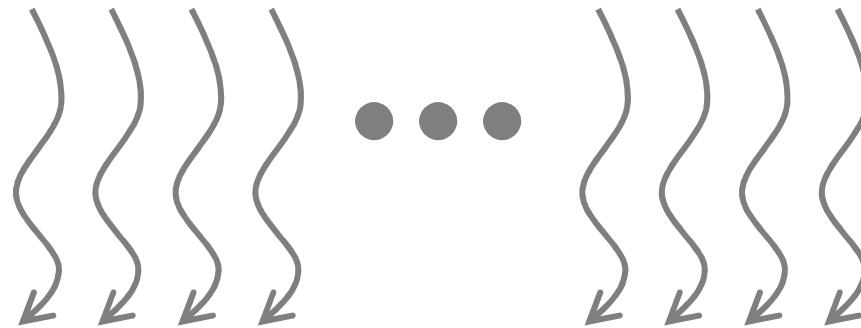


Memory

# Data Packing

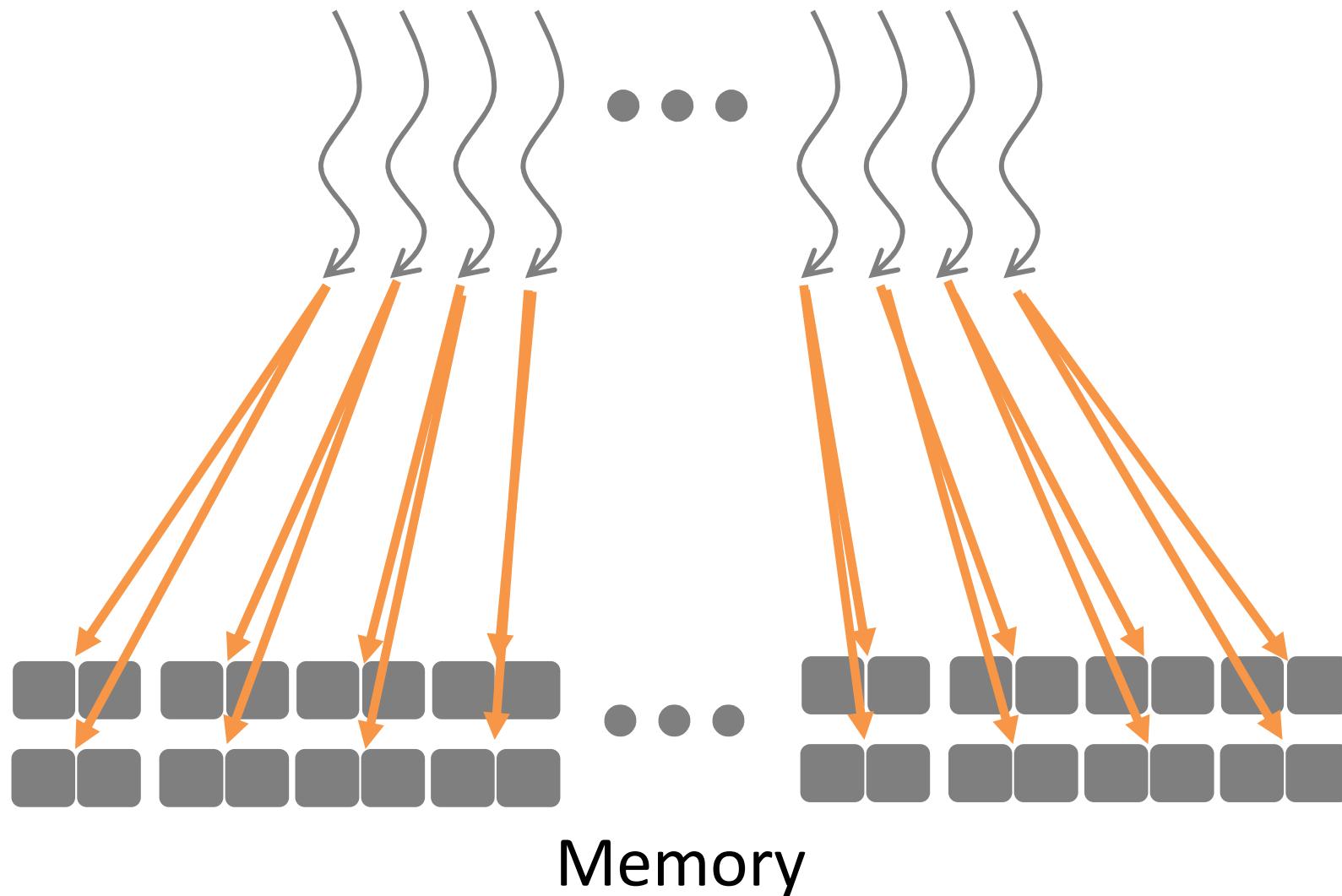
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Threads



# Data Packing

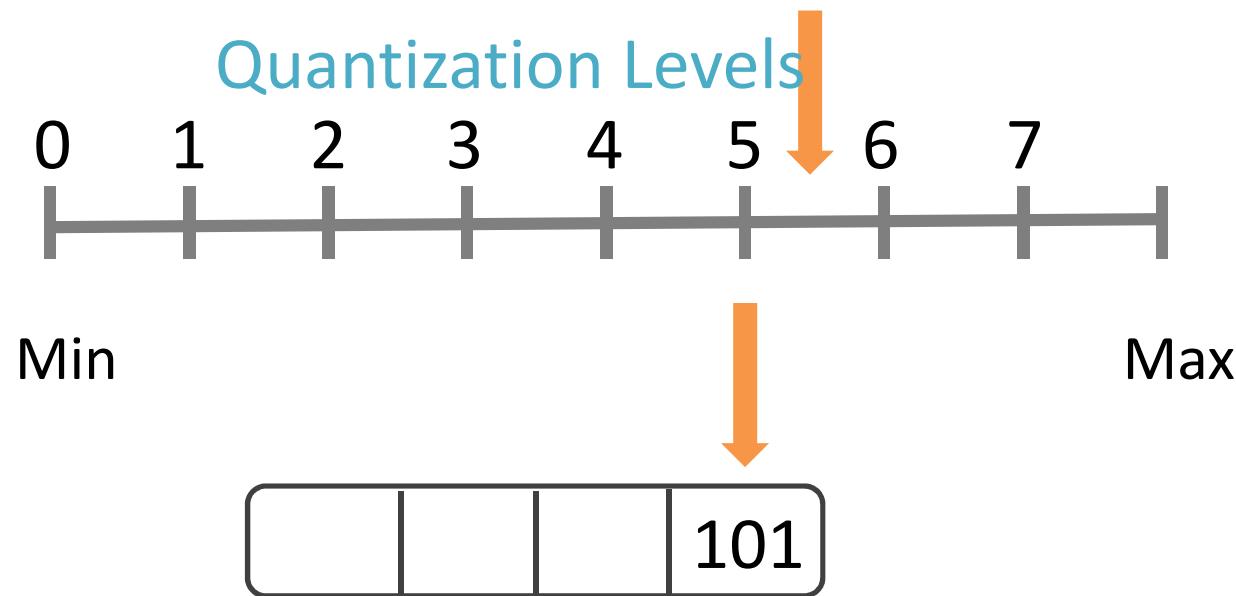
Threads



# Quantization

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- Preprocessing finds min and max of the input sets and packs the data

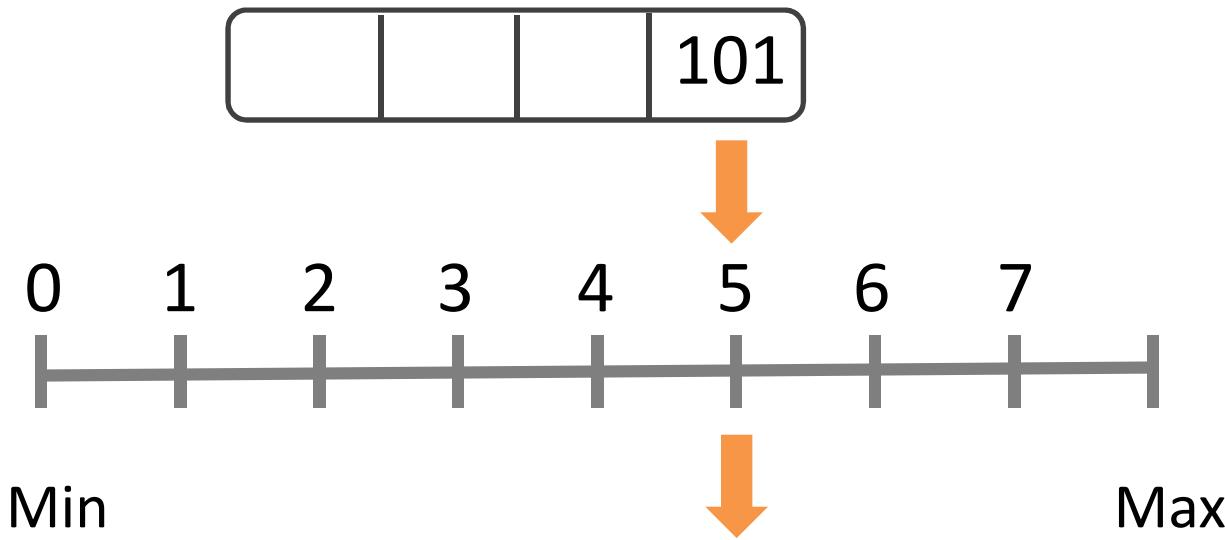


- During execution, each thread unpacks the data and transforms the quantization level to data by applying a linear transformation

# Quantization

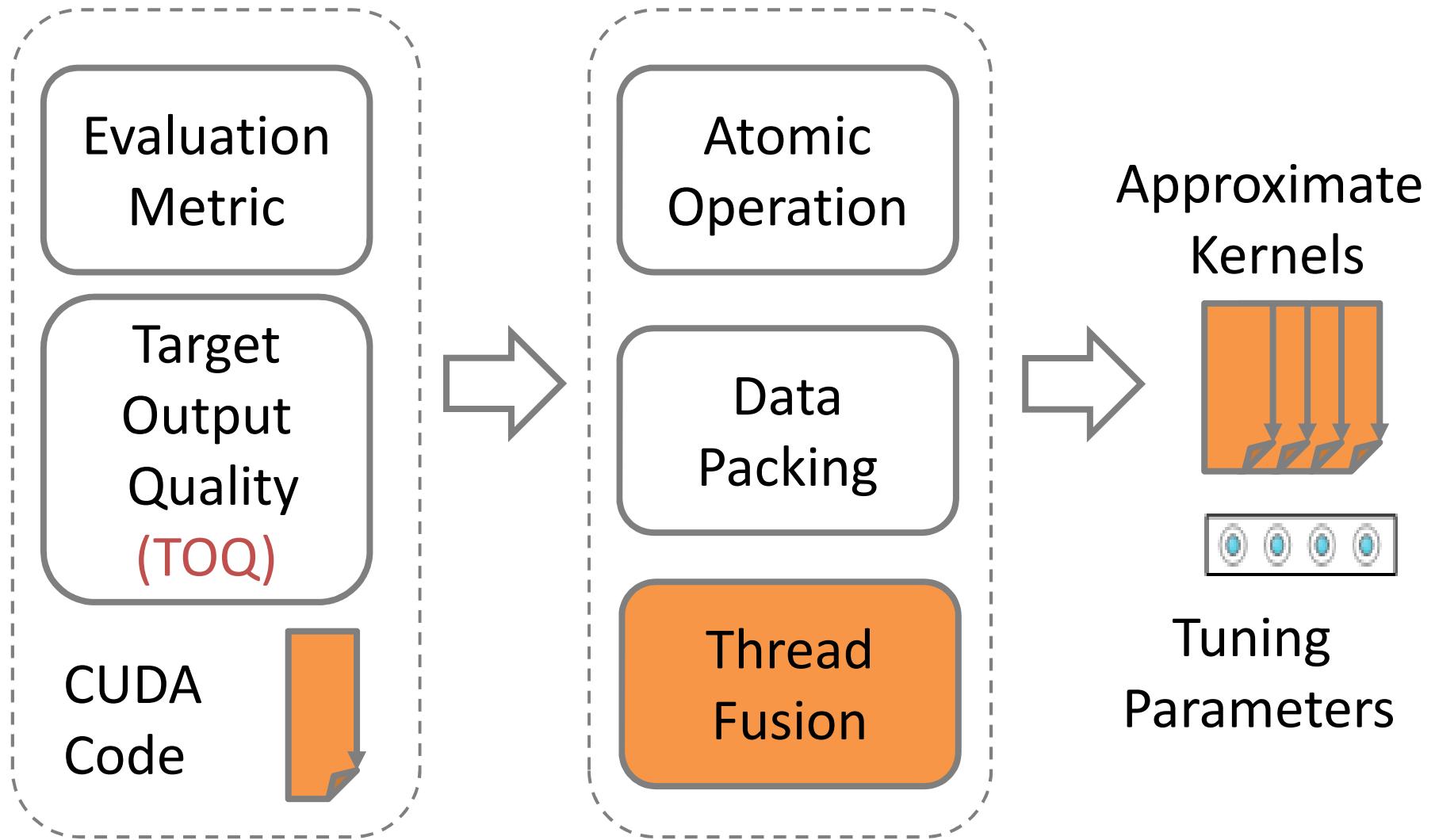
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- Preprocessing finds max and min of the input sets and packs the data



- During execution, each thread unpacks the data and transforms the quantization level to data by applying a linear transformation

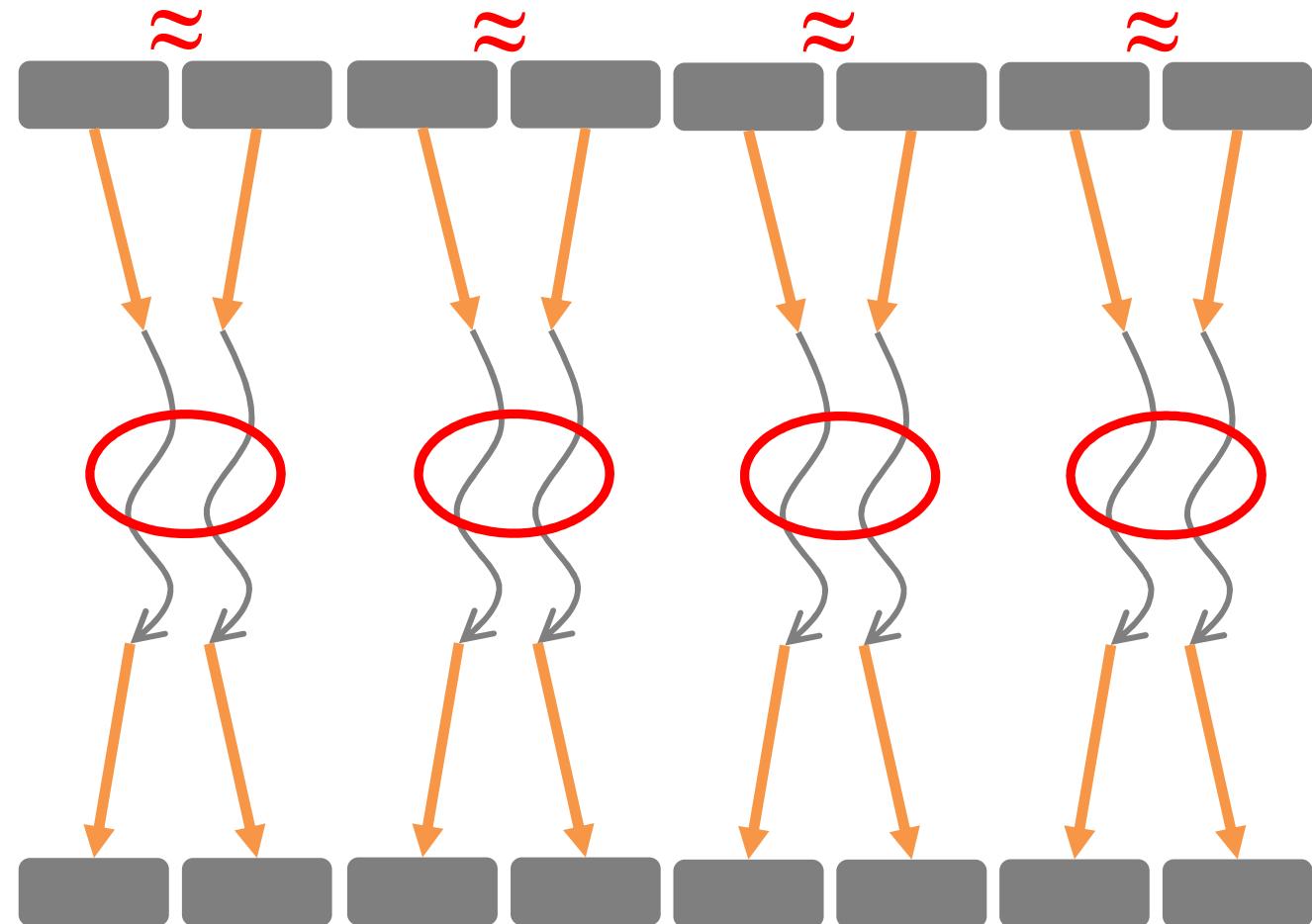
# Approximation Methods



# Thread Fusion

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Memory



Threads

Memory

# Thread Fusion

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Memory



Threads



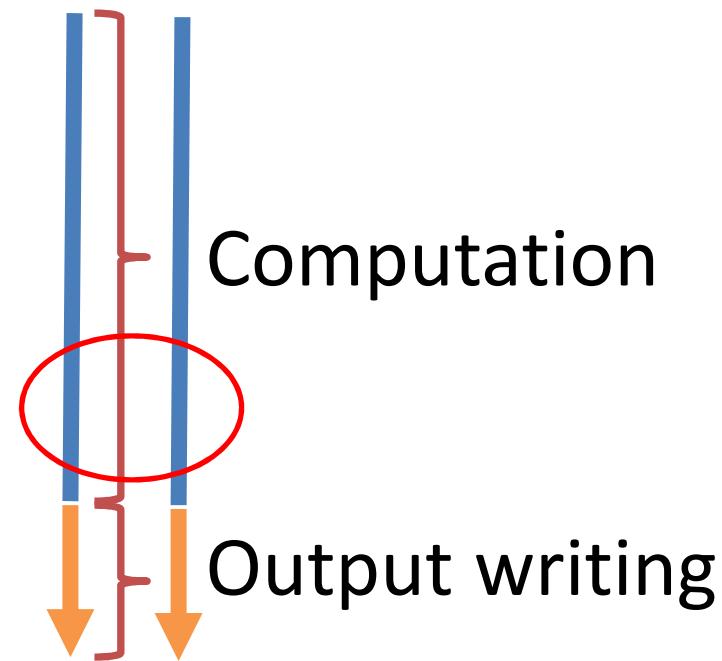
Memory



# Thread Fusion

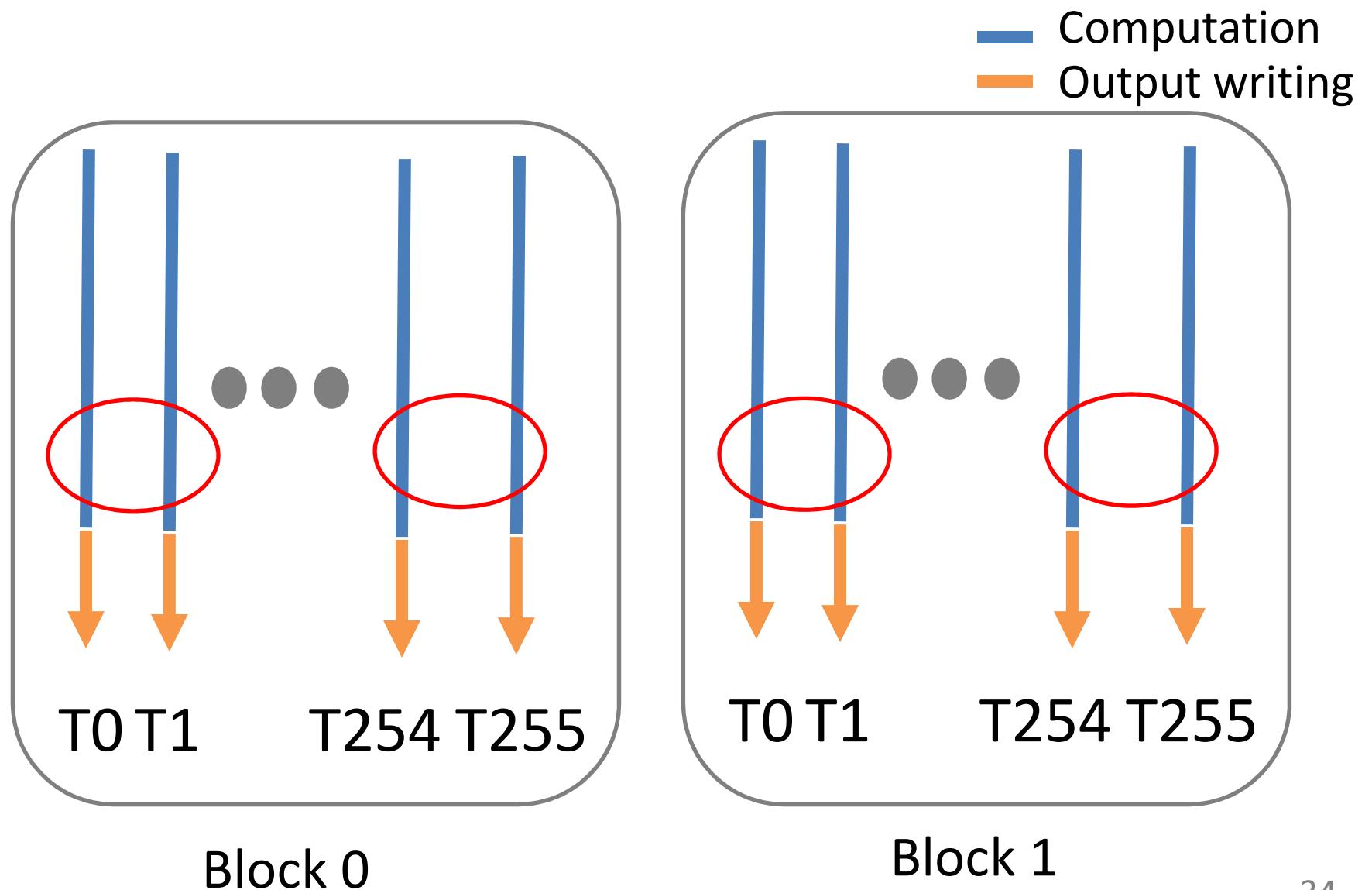
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■ Computation  
■ Output writing

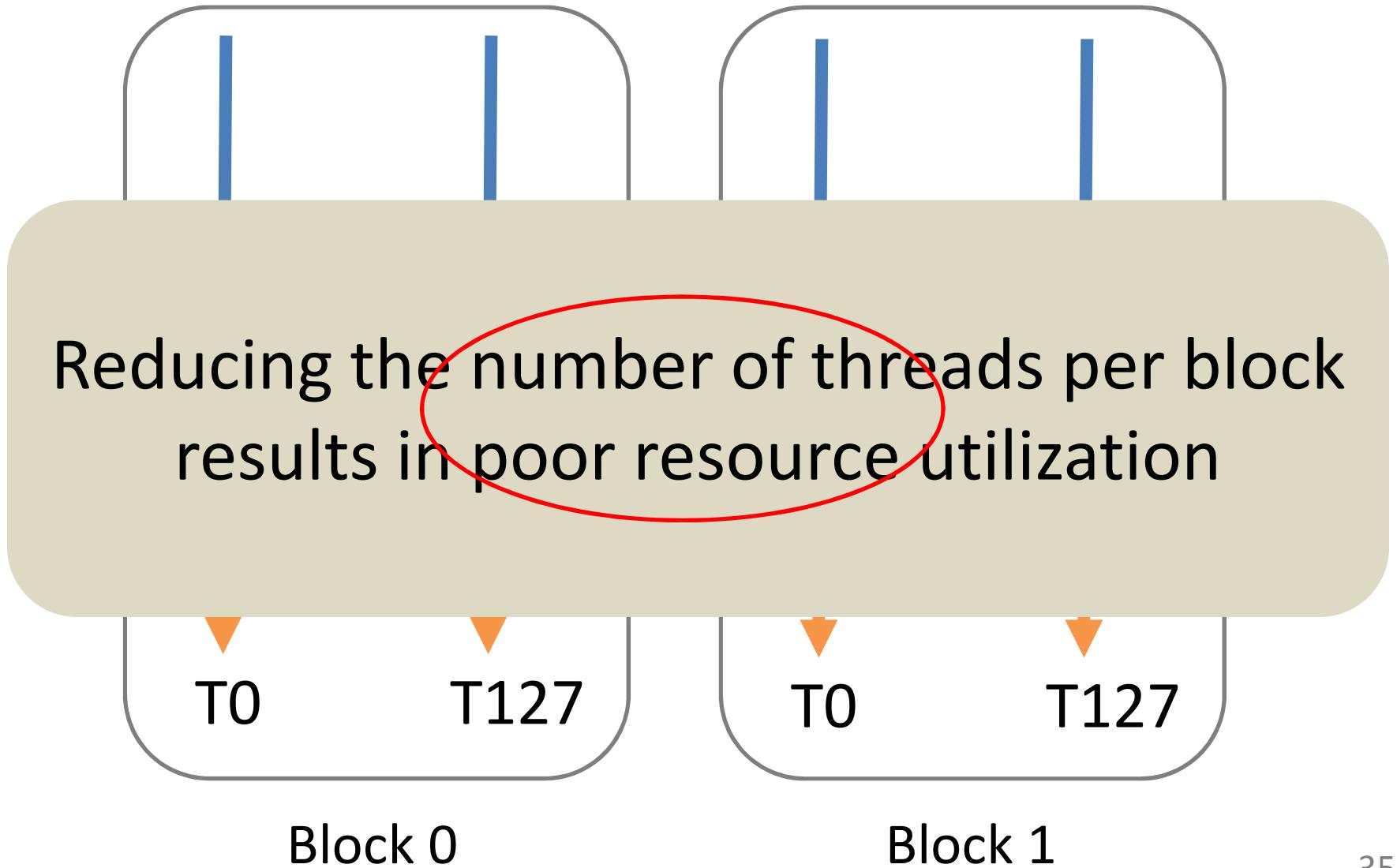


T0 T1

# Thread Fusion

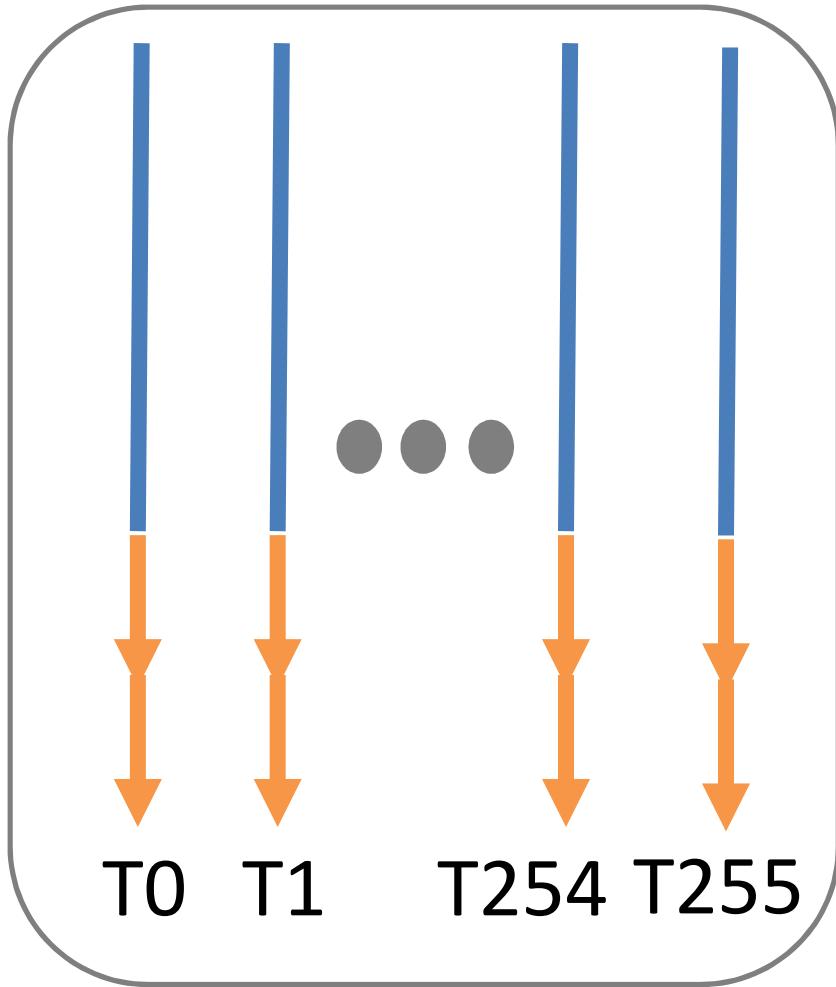


# Thread Fusion



# Block Fusion

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Block 0 & 1 fused

---

# Runtime

# How to Compute Output Quality?

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Approximate  
Version



Accurate  
Version



Evaluation  
Metric

- High overhead
- Tuning should find a good enough approximate version as fast as possible
- Greedy algorithm

# Tuning

---

**TOQ = 90%**

$K(0,0)$

Quality = 100%

Speedup = 1x

Tuning parameter of the  
**First** optimization

Tuning parameter of the  
**Second** optimization

$K(x,y)$



# Tuning

---

**TOQ = 90%**

94%  
1.15X

K(1,0)

K(0,0)

Quality = 100%  
Speedup = 1x

96%  
1.5X

# Tuning

---

**TOQ = 90%**

94%  
1.15X

K(1,0)

K(0,0)

Quality = 100%  
Speedup = 1x

96%  
1.5X

K(0,1)

94%  
2.5X

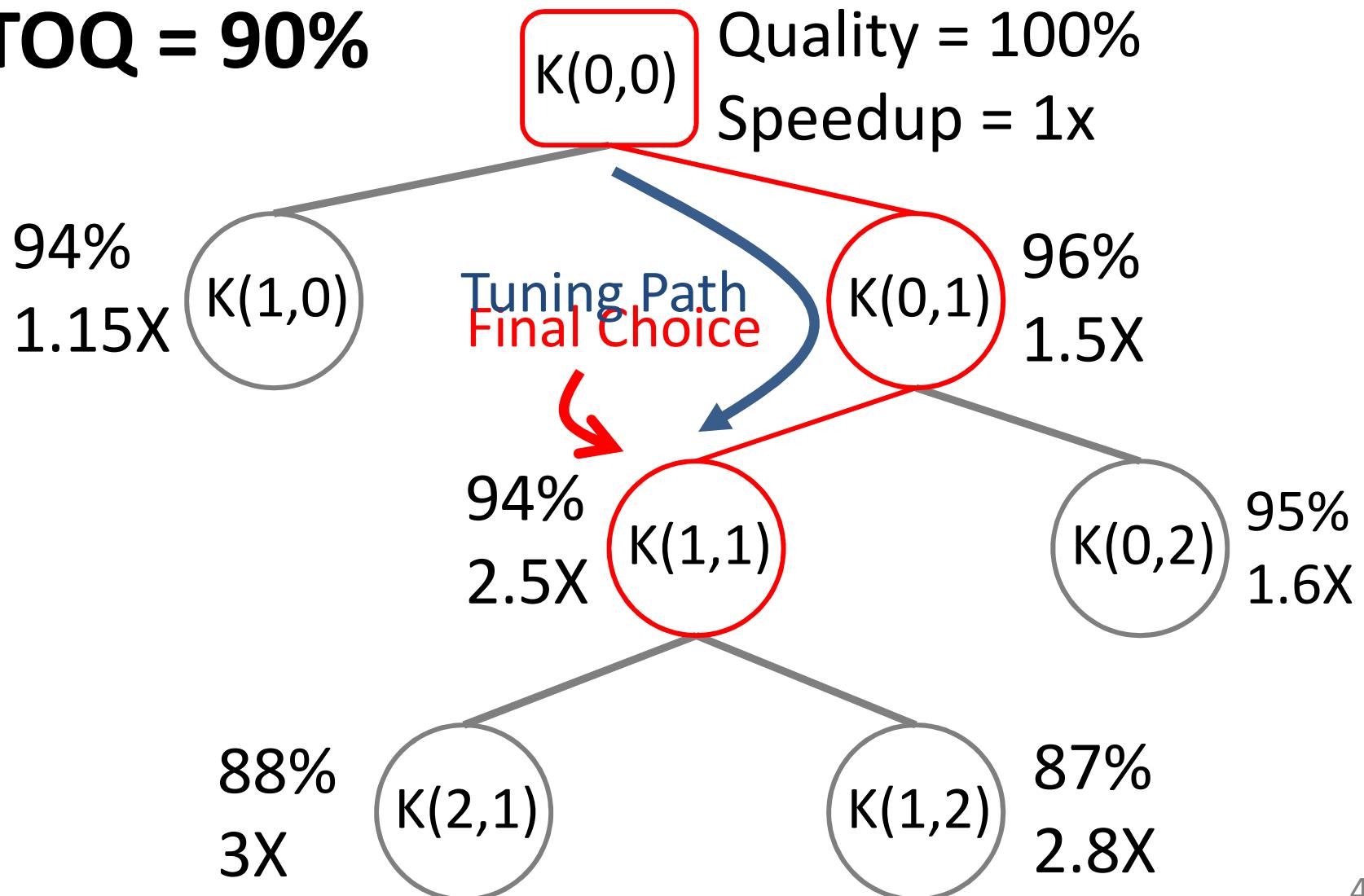
K(1,1)

K(0,2)

95%  
1.6X

# Tuning

**TOQ = 90%**



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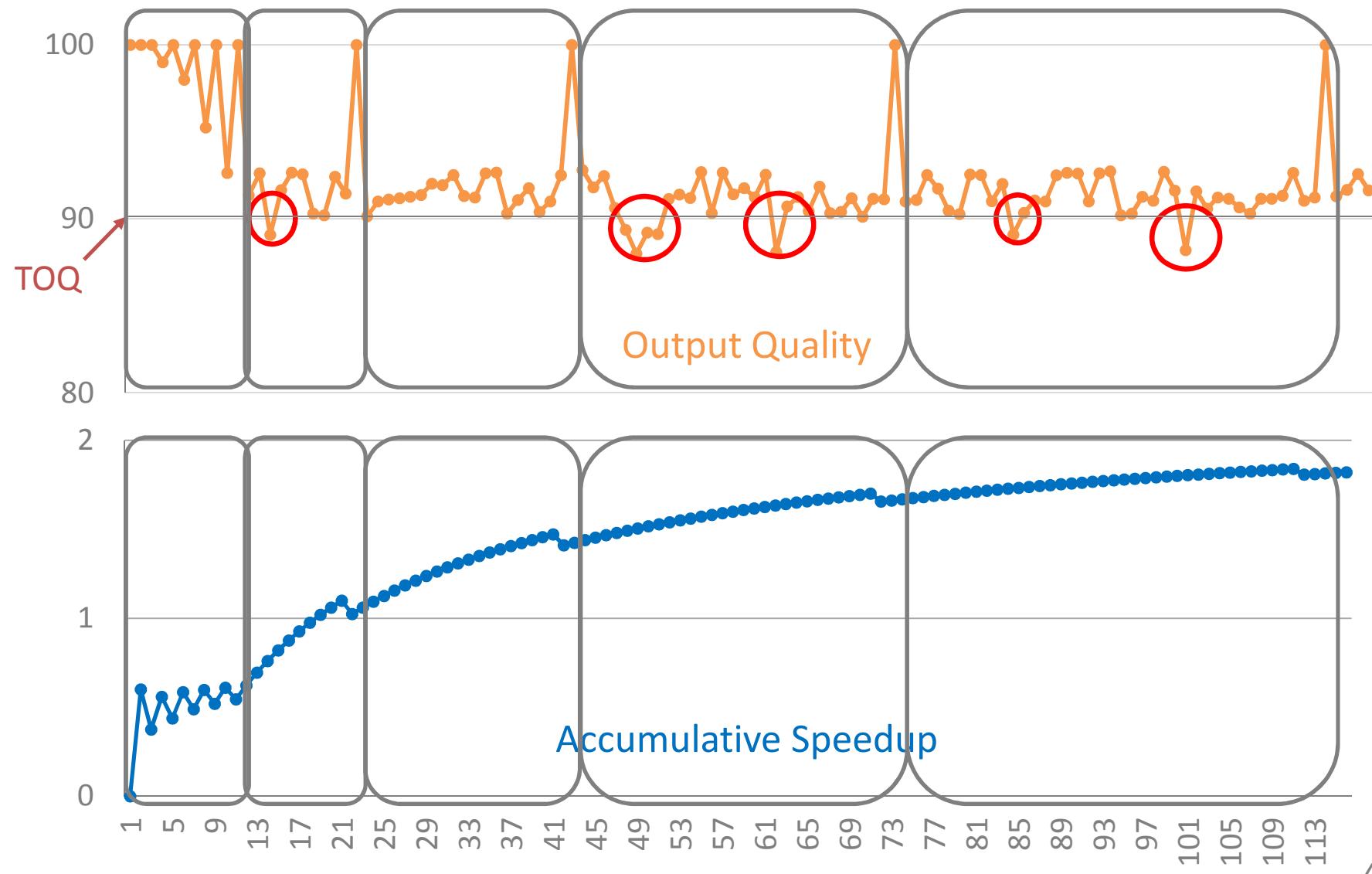
# Evaluation

# Experimental Setup

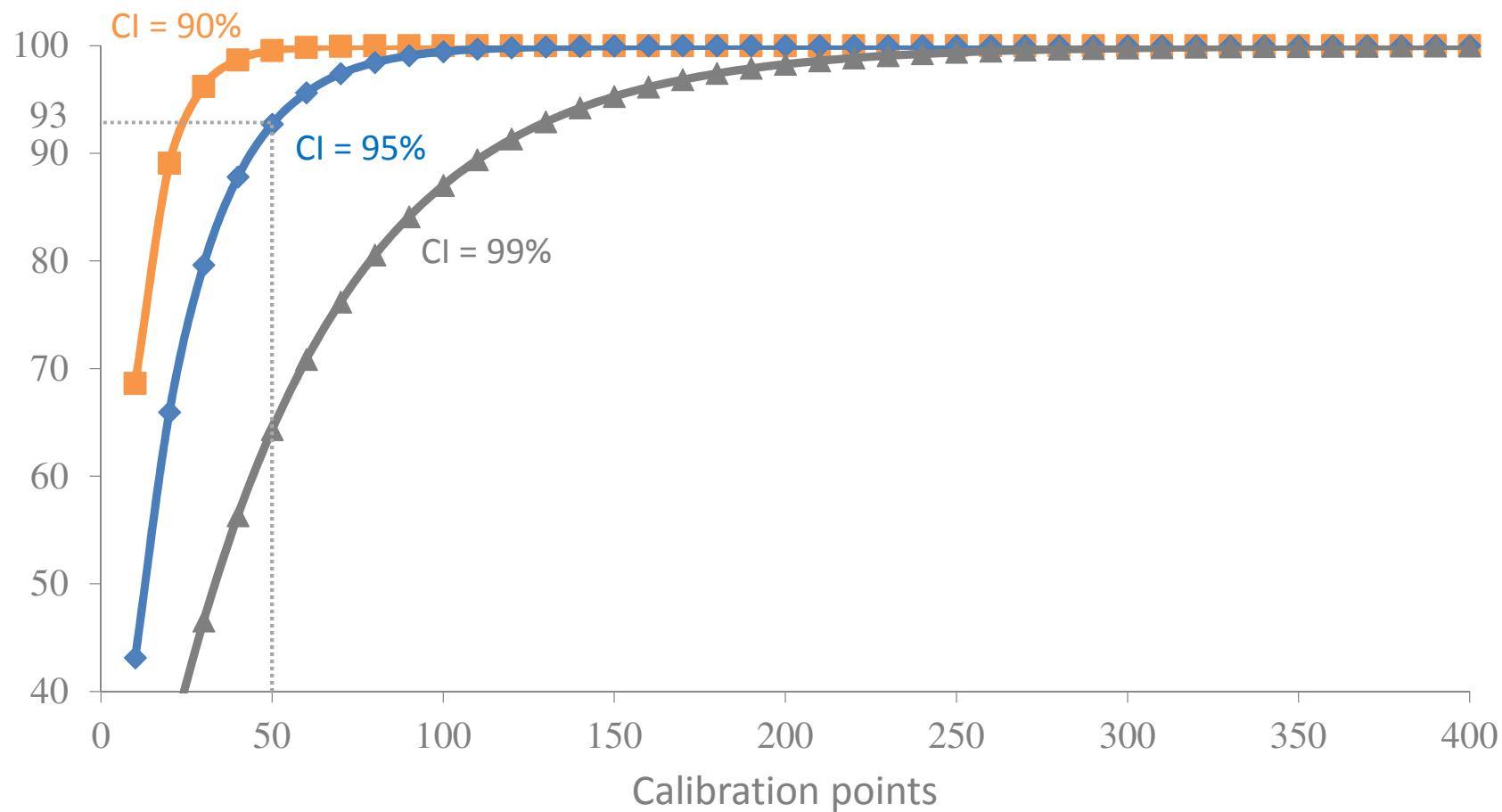
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- Backend of Cetus compiler
- GPU
  - NVIDIA GTX 560
    - 2GB GDDR 5
- CPU
  - Intel Core i7
- Benchmarks
  - Image processing
  - Machine Learning

# K-Means



# Confidence



After checking 50 samples, we will be 93% confident that 95% of the outputs satisfy the Q threshold

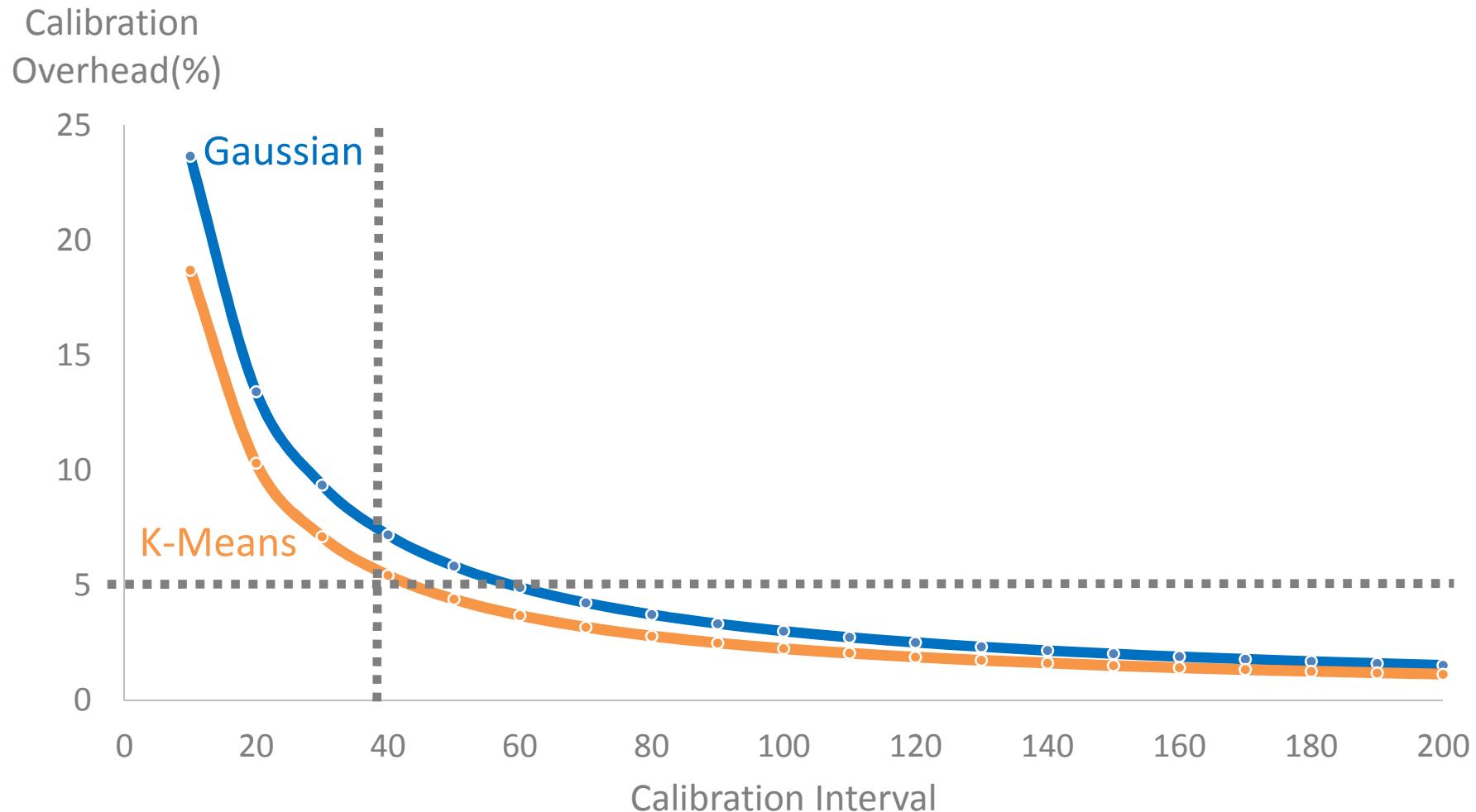
Beta

Uniform

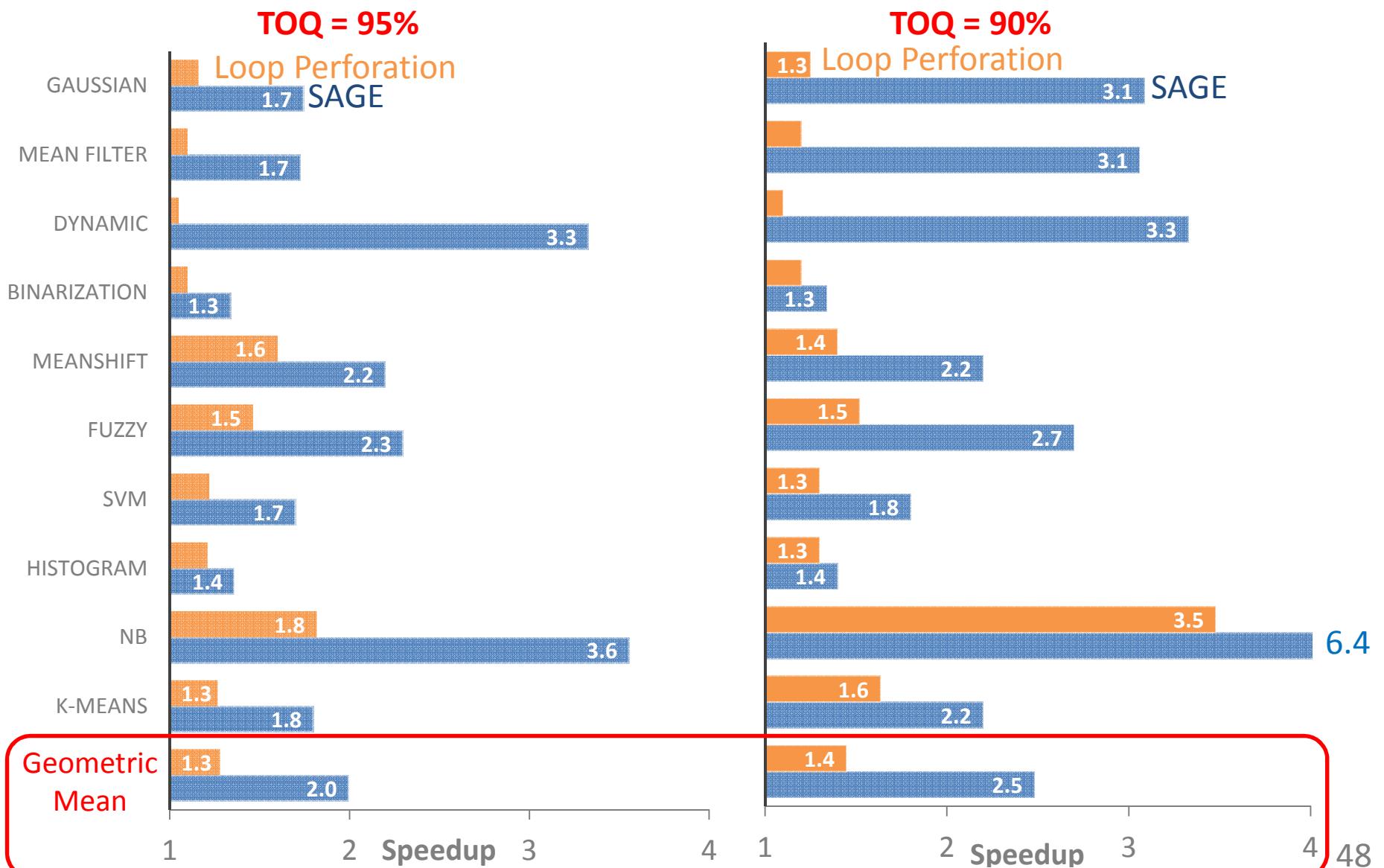
Binomial

# Calibration Overhead

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# Performance



# Conclusion

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- Automatic approximation is possible
- SAGE automatically generates approximate kernels with different parameters
- Runtime system uses tuning parameters to control the output quality during execution
- 2.5x speedup with less than 10% quality loss compared to the accurate execution

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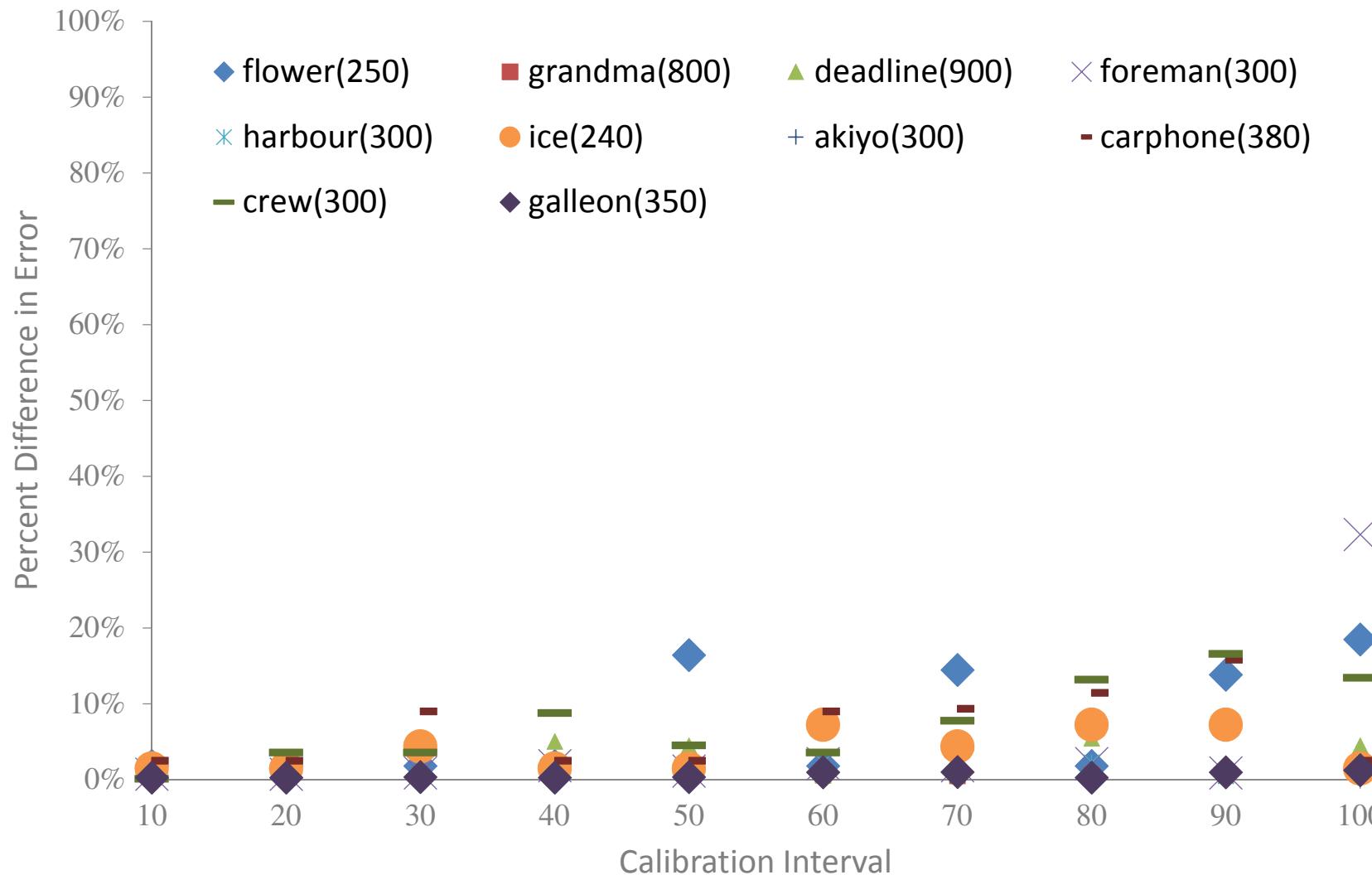


Compilers creating custom processors

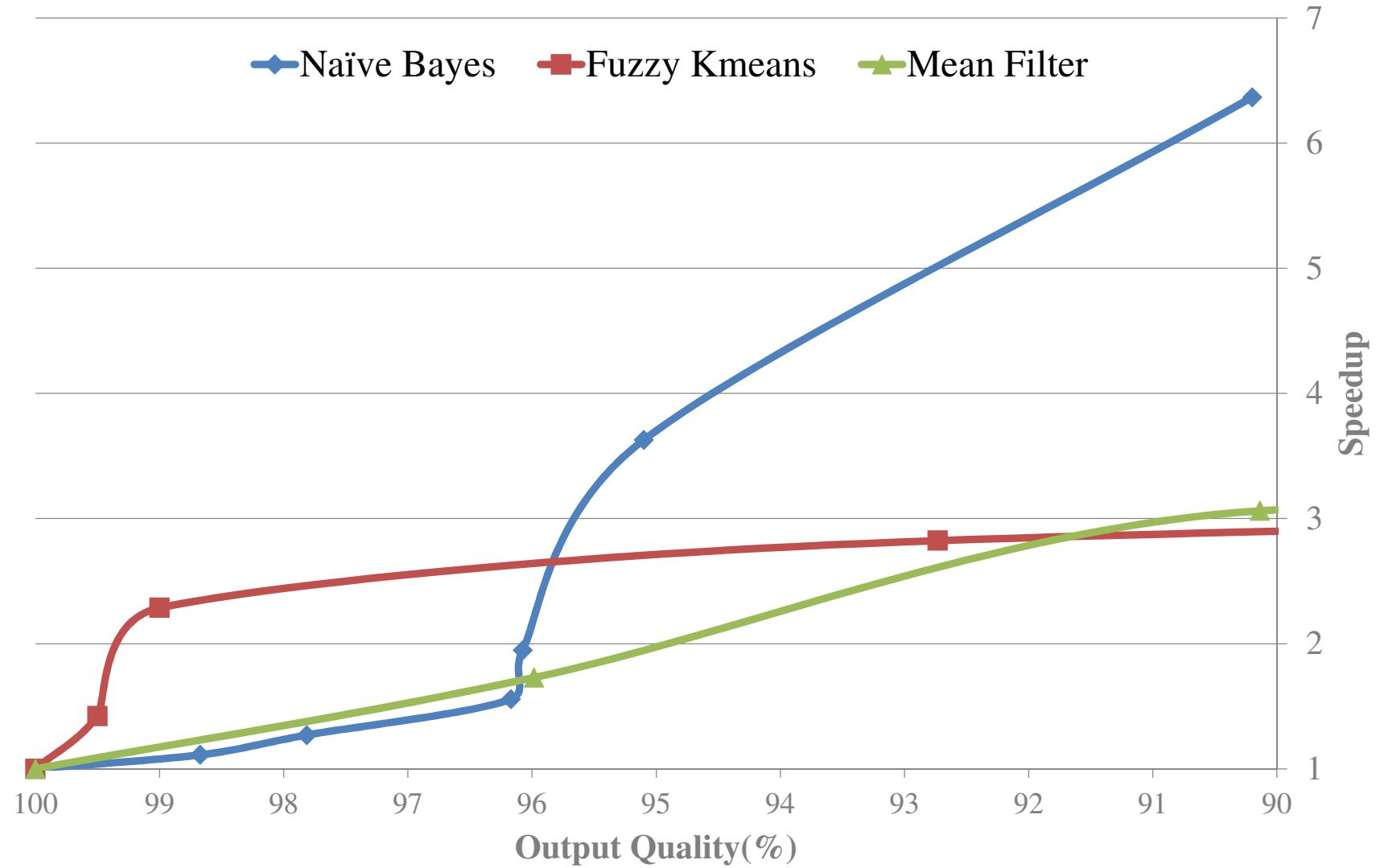
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# Backup Slides

# What Does Calibration Miss?



# SAGE Can Control The Output Quality



# Distribution of Errors

