



CALL FOR PAPERS

MICRO - 42

December 12-16, 2009 New York City
The 42nd Annual IEEE/ACM International
Symposium on Microarchitecture

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The International Symposium on Microarchitecture is the premier forum for presenting, discussing, and debating innovative microarchitecture ideas and techniques for advanced computing and communication systems. This symposium brings together researchers in fields related to microarchitecture, compilers, chips, and systems for technical exchange on traditional microarchitecture topics and emerging research areas. The MICRO community has enjoyed a close interaction between academic researchers and industrial designers and we aim to continue this tradition at MICRO-42. Papers are solicited in fields including (but not limited to) the following:

- Architectures and designs for concurrency
- Compiler techniques for instruction-, thread-, and memory- level parallelism
- Architectures/compilers for graphics, gaming, embedded apps, DSPs, ASIPs
- Dynamic optimization, emulation, and object code translation
- Software/hardware speculation and prediction schemes
- Low and ultra-low power designs, design efficiency optimizations
- Microarchitecture support for reliability, dependability, and security
- Microarchitecture modeling and simulation methodology
- Architectures for new computing paradigms/emerging technologies
- Measurement and analysis of real systems
- Novel memory and storage architectures
- Novel interconnection fabrics, NOCs, optical, etc.

SUBMISSION DEADLINES: Paper abstracts are due on May 15, 2009. Final paper submissions are due on May 22, 2009. All submissions will be made electronically and no extensions will be granted. For more information please consult the conference website.

Important Dates:

Abstract Submission Deadline: May 15, 2009

Full Paper Submission Deadline: May 22, 2009

Author Rebuttal Deadline: July 23, 2009 8 am PDT

Author Notification Date: July 28, 2009

<http://www.microarch.org/micro42>