The 39th International Symposium on Microarchitecture is the premier forum for presenting, discussing, and debating new and innovative microarchitecture ideas and techniques for advanced computing and communication systems. The goals of this symposium are to bring together researchers in fields related to microarchitecture, compilers, and systems for technical exchange on traditional microarchitectural topics as well as emerging research areas. Historically, the MICRO community has enjoyed having close interaction between academic researchers and industrial designers; we aim to continue and emphasize this tradition at MICRO-39. Papers are solicited in fields including the following:

- ILP and TLP architectures and designs: superscalar, VLIW, multithreaded, multicore, etc.
- Compiler techniques for instruction-level, thread-level, and memory-level parallelism
- Architectures and compilers for embedded processors, DSPs, ASIPs (network processors, multimedia, wireless, …)
- Dynamic optimization, emulation, and object code translation
- Advanced software/hardware speculation and prediction schemes
- Power, performance, and implementation efficient designs
- Microarchitecture support for reliability, dependability, and security
- Microarchitecture modeling and simulation methodology

Qualitative papers: The MICRO-39 conference, in addition to the usual papers, will emphasize papers that focus on qualitative presentations of more forward looking ideas. Such papers will be evaluated more on novelty, creativity, and inherent value propositions, and less on implementation and experimental evaluation. Authors are encouraged to submit more unconventional ideas (though still within the scope of MICRO) with this class of paper. Qualitative papers should provide some indication as to the potential merits of the proposed ideas, such as preliminary evaluations that extrapolate the potential. Both classes of papers will be considered equally for inclusion based on their overall merit.

THE DEADLINE FOR SUBMISSIONS IS JUNE 2, 2006. There is an automatic 1-week extension for late papers (June 9), but no other extensions. Please submit one electronic copy of the paper in PDF format. For more information please consult the conference website: http://www.microarch.org/micro39